Propose Scheme of Instruction, Evaluation

For Four year B.Tech program
In Electrical & Communication Engineering
With effect from Academic Year
2025-26

DEPARTMENT OF ELECTRICAL AND COMMUNICATION ENGINEERING

UNIVERSITY COLLEGE OF ENGINEEIRNG & TECHNOLOGY

MAHATMA GANDHI UNIVERSITY

Nalgonda - 508001, TS, INDIA

SCHEME OF INSTRUCTION AND EVALUATION

B. TECH (ELECTRONICS AND COMMUNICATION ENGINEERING)

(With effect from AY: 2025-26)

V-SEMESTER

SNo	Code	Course Title	Scheme of Instruction			Contact Hrs/ Wk	Scheme of Evaluation			Credits
			L	T	P		Hrs	CIE	SEE	
Theory										
1	PC501EC	Antenna and Wave Propagation	3	-	-	3	3	30	45	3
2	PC502EC	Digital System Design using Verilog HDL	3	-	-	3	3	30	45	3
3	PC503EC	Digital Communication	3	-	-	3	3	30	45	3
4	PC504EC	Linear Control Systems	3	-	-	3	3	30	45	3
5	PC505EC	Micro-controllers and Interfacing	3	-	-	3	3	30	45	3
6	PC506EC	Computer Architecture and Organization	3	-	-	3	3	30	45	3
7	**Professional Elective-II**		3	-	-	3	3	30	45	3
	PE511CS	Artificial Intelligence & Machine Learning								
	PE512EC	Digital Image Processing								
	PE513CS	Object Oriented Programming using C++								
Practical's										
8	PC551EC	Digital Communication Lab	-	-	2	2	3	20	30	1
9	PC552EC	Digital System Design Lab	-	-	2	2	3	20	30	1
		Total	21	-	4	25	27	250	375	23

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Course Code	Course Title						Core//PE/OE
PC501EC	ANTENNA AND WAVE PROPAGATION						Core
Pre-requisites	Contact Hours Per Week				CIE	SEE	Credits
-	L	T	D	P			
	3	-	-	-	30	45	3

Course Objectives

1. To understand the various antenna parameters to give insight of the radiation phenomena.
2. To have thorough understanding of radiation characteristics of different types of antennas.
3. To study the characteristics of array antennas having directional radiation characteristics.
4. To get insight on aperture antennas and modern antennas.
5. To understand the concepts of wave propagation and create awareness about the different types of propagation of radio waves at different frequencies.

Course Outcomes

1. Acquires knowledge about the basic antenna parameters and radiation concepts.
2. Analyze wire antennas in detail.
3. Attain engineering fundamentals to analyze and design antenna arrays.
4. Classify, analyze and design aperture and modern antennas.
5. Identify and explain modes of propagation in different regions of atmosphere.

UNIT–I

Fundamentals of Antenna theory: Principle of radiation, Basic Antenna Parameters – Patterns, Beam Area, Radiation Intensity, Beam Efficiency, Directivity, Gain, Antenna Apertures, Effective Height, Illustrative Problems. Retarded Potentials – Helmholtz Theorem. Thin Linear Wire Antennas – Radiation from Small Electric Dipole, Quarter Wave Monopole and Half Wave Dipole – Current Distributions, Near field and Far field Components, Radiated Power, Radiation Resistance, Beam Width, Directivity, Effective Area and Effective Height. Loop Antennas – Introduction, Small Loop, Comparison of Far Fields of Small Loop and Short Dipole.

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UNIT–II

Antenna Arrays: Basic two element array, N element uniform linear array, Pattern multiplication, Broadside and End fire array, Planar array, Concept of Phased arrays, Basic principle of antenna Synthesis-Binomial array, Tschebysev array.

UNIT–III

Practical Antennas: Yagi-Uda antenna, V-Antenna, Rhombic antenna, Travelling wave antennas. Microstrip antennas– Introduction, Features, Advantages and Limitations, Rectangular Patch Antennas – Geometry, Design equations and Characteristics.

UNIT-IV

Aperture and Modern Antennas: - Reflector Antennas – Introduction, Flat Sheet and Corner Reflectors. Parabolic Reflectors – Geometry, Pattern Characteristics, Feed Methods, Reflector – Types. Related Features, Illustrative Problems. Horn Antennas – Types, Fermat's Principle, Radiation from sectorial and pyramidal horns, Design Considerations of Pyramidal Horns. Reconfigurable antenna, Active antenna, Dielectric Antennas, Electronic band gap structure and applications.

UNIT – V

Wave propagation: Ground wave propagation. Space and surface waves, Tropospheric Refraction and reflection. Sky wave propagation – Virtual height, critical frequency, Maximum usable frequency – Skip distance, Fading, Multi-hop propagation.

Suggested Reading:

1 Constantine A. Balanis, Modern Antenna Handbook, A John Wiley & Sons, Inc., Publication, 2008.

2 John D.Kraus, Ronald J.Marhefka and Ahmed S.Khan, Antennas for All Applications, 3rd Edition, Tata McGraw- Hill publishing company Limited, New Delhi, 2006.

3 K.D.Prasad, "Antennas and Wave Propagation", Khanna or Satya Publications.

4 Warren L. Stutzman, Gary A. Thiele, Antenna Theory and Design, 3rd Edition. May 2012

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Course Code	Course Title						Core//PE/OE
PC502EC	DIGITAL SYSTEM DESIGN USING VERILOG HDL						Core
Pre-requisites	Contact Hours Per Week				CIE	SEE	Credits
STLD	L	T	D	P			
	3	-	-	-	30	45	3

Course Objectives:

The course is taught with the objectives of enabling the student to:

1. Familiarize with structural modeling with different design approaches and writing test.
2. Familiarize with behavioral modeling of digital systems using Verilog HDL.
3. Understand synthesis of various sub systems.
4. Familiarize with various ICs available (combinational units) and their usage and to design.
5. Understand FSM coding.

Course Outcomes:

On completion of this course, the student will be able to:

1. Develop structural designs in top-down and bottom-up approach and develop test benches for the same.
2. Develop combinational and sequential circuits in data flow and behavioral modeling styles.
3. Understand the various language constructs and the corresponding hardware implementation (Synthesis).
4. Familiarize with commercially available ICs of various combinational and sequential building blocks.
5. Develop Verilog code for FSMs and FSMDs.

UNIT–I

Introduction To Logic Design With Verilog: Structural models of combinational logic: Verilog primitives, design encapsulation, structural models, module ports and structural connectivity, Top-down and nested modules, design hierarchy, vectors in Verilog. Four valued logic and signal resolution in Verilog, test methodology, Signal generators for test benches, test bench templates, propagation delay and truth table models of Combinational and sequential logic with Verilog.

UNIT–II

Logic Design With Behavioral Models Of Combinational And Sequential logic: Data types, continuous assignment, Boolean equation based

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behavioral models of combinational logic-multiplexers, encoders, decoders, modeling flip-flops and latches, edge detection, LFSRs, Modeling with repetitive algorithms(loops), clock generators, behavioral models of counters, registers, register files and Array of registers(Memories)

UNIT-III

Synthesis Of Combinational And Sequential Logic - Introduction to synthesis - Logic synthesis, RTL synthesis, High-level synthesis, Synthesis of combinational logic, synthesis of sequential logic with latches and flip flops, synthesis of loops.

Introduction to VLSI RTL designs: RTL designs- Goals and Constraints, RTL based chip design flow and design challenges

UNIT-IV

Combinational Logic Ics – Specifications and Applications of TTL-74XX MSI ICs - Decoders, BCD- seven segment display Decoders with Drivers, Encoders, Priority Encoders, Multiplexers, Parity Generators/Checkers, Parallel Binary Adder/Subtractor and Magnitude Comparators

Sequential Logic Ic's: Familiarity with commonly available TTL 74XX, CMOS 40XX Series ICs – Asynchronous and synchronous Counters, Decade Counters, Shift Registers.

UNIT – V

Finite State Machines: Introduction, Mealy and Moore Outputs, FSM representation- state diagram and ASM chart, FSM code development and design examples.

Fsm With Data Path (Fsmd): Introduction, single RT operation, ASMD chart , Decision box realization with register, code development for FSMD with design examples.

Suggested Reading:

1. Michael D. Ciletti , "Advanced digital design with Verilog HDL", PHI learning Pvt Ltd, 2012
2. Samir Palnitkar, *"Verilog HDL A Guide to Digital Design and Synthesis,"* 2nd Edition, Pearson Education, 2006.
3. Sanjay Churiwala · Sapan Garg "Principles of VLSI RTL design- A practical guide", , Springer, 2010
4. R.P.Jain, *"Modern Digital Electronics"*, Tata McGraw Hill, 4th Edition, 2009.
5. Pong P Chu, "FPGA Proto Typing by Verilog Examples" WILEY Publications

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Course Code	Course Title						Core//PE/OE
PC503EC	DIGITAL COMMUNICATION						Core
Pre-requisites	Contact Hours Per Week				CIE	SEE	Credits
-	L	T	D	P			
	3	-	-	-	30	45	3

Course Objectives:

The course is taught with the objective of enabling the student to

1. Understand the building blocks of digital communication systems and waveform coding techniques.
2. Get familiarized with various source coding techniques and Block codes.
3. Get familiarized with convolution and cyclic codes.
4. Analyze various digital carrier modulation techniques.
5. Understand the concept of spread spectrum modulation.

Course Outcomes:

On completion of this course, the student will be able to:

1. Understand the basic components of digital communication systems
2. Understand how to design block codes, convolution, and cyclic codes
3. Apply suitable digital carrier modulation techniques and coding techniques for various applications for improved spectral efficiency
4. Learn to design an optimum receiver and analyze the error performance of baseband and band pass data transmission.
5. Analyze the performance of the spread spectrum communication system.

UNIT–I

Digital Transmission of Analog Signals: Elements of digital communication system, Sampling theory, Quantizing of Analog Signals, Coded Transmission of Analog Signals: PCM, Differential PCM, Delta Modulation, Noise in PCM, DM system. Performance comparison of the above systems.

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UNIT–II

Source Coding: Introduction, Shannon-Fano Coding, Huffman Coding.

Linear Block Codes: Introduction, Matrix description of Linear Block codes, Error Detection and Error Correction capabilities of linear block codes, Single Error Correcting Hamming codes.

UNIT–III

Binary Cyclic Codes: Algebraic Structure of Cyclic Codes, Encoding, Syndrome Calculation, , Error Detection and Error Correction, BCH Codes

Convolution Codes: Introduction, Encoding of convolution codes, Graphical approach: State, Tree and Trellis diagram, The Viterbi algorithm. Comparison of the above codes.

UNIT-IV

Digital Band-Pass Modulation Techniques: Binary Amplitude-Shift Keying, Phase-Shift Keying. Frequency-Shift Keying, Summary of Three Binary Signaling Schemes, Noncoherent Digital Modulation Schemes, M-ary Digital Modulation Schemes, Mapping of Digitally Modulated Waveforms onto Constellations of Signal Points.

Bit Error Rate, Detection of a Single Pulse in Noise, Optimum Detection of Binary PAM in Noise, Optimum Detection of BPSK, Detection of QPSK and QAM in Noise, Optimum Detection of Binary FSK.

UNIT – V

Spread Spectrum Modulation: Introduction, Generation and Characteristics of PN- sequences. Direct Sequence Spread Spectrum system; Frequency Hopping spread spectrum system and their application, acquisition scheme for spread spectrum receivers, tracking of FH and DS signals.

Suggested Reading:

1. K Sam Shanmugam, *"Digital and Analog Communication Systems"*, John Wiley &sons, 1979.
2. John G. Proakis, *"Digital Communications"*, 4th Edition, Tata McGraw- Hill Publishing Company Limited, New Delhi, 2003.
3. Rodger E.Ziemer, William H.Tranter, "Principles of Communications-Systems, Modulation and Noise", 7th Edition, Wiley, 2014.

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Course Code	Course Title						Core//PE/OE
PC504EC	LINEAR CONTROL SYSTEM						Core
Pre-requisites	Contact Hours Per Week				CIE	SEE	Credits
	L	T	D	P			
Networks, Laplace Transforms	3	-	-	-	30	45	3

Course Objectives:

The course is taught with the objectives of enabling the student to:

1. To develop mathematical modeling for different control systems.
2. To construct state space model for continuous and discrete data systems and analyze them.
3. To analyze control system in time domain and determine stability using Routh-Hurwitz criterion and Root-Locus technique.
4. To analyze control system in frequency domain and determine stability using Nyquist criterion and bode plots.
5. To design compensators for control systems.

Course Outcomes:

On completion of this course, the student will be able to:

1. Able to develop mathematical models and derive transfer functions for various systems.
2. Able to expose to an appropriate state space modeling of system and its analysis and the concept and testing of controllability and observability.
3. Able to analyze the systems in time domain and determine its stability.
4. Able to analyze the systems in frequency domain and determine relative stability.
5. Able to design compensators for a given specifications

UNIT–I

Introduction to control systems: Basic components, classification of control systems, effects of feedback, mathematical modeling of physical systems, - Mathematical modelling of control systems - Electrical Systems and mechanical translational systems - transfer function – Electrical analogous of mechanical translational and Rotational systems -Block diagrams representation and reduction methods - signal flow graph - mason's gain formula

UNIT–II

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State-variable analysis of continuous data systems: state, state variables, state equations, solution of state equations, state transition matrix and its properties, state diagram, relationship between state equations and transfer functions, concept and testing of controllability and observability.

UNIT–III

Analysis of continuous time systems : time domain solution of first order systems – time constant, time domain specifications - time domain solution of second order systems – damping ratio - response of second order systems for step input - steady state error and static error coefficients for standard inputs - concept of stability –location of roots on the s plane - Routh-Hurwitz techniques - construction of root locus.

UNIT-IV

Frequency-domain analysis: Introduction to Frequency domain specifications, Relationship between time and frequency response, Nyquist stability criterion, Bode plots, relative stability – gain margin and phase margin.

UNIT – V

Design of control systems: Phase lag, phase lead and phase Lag-Lead compensators and their design. Controllers: Introduction to PI, PD and PID controllers.

Suggested Reading:

1. I.J.Nagrath and M Gopal, "Control System Engineering", New Age International Private Limited, New Delhi, 2008, 5th Edition
2. Katsuhiko Ogata, "Modern Control Engineering", Prentice-Hall of India Private Limited, New Delhi, 2003, 4th Edition.
3. Benjamin C. Kuo, "Automatic Control Systems", Prentice Hall of India, 2009, 7th Edition
4. Control Systems Engineering by A. Nagoor Kani, RBA Publications

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Course Code	Course Title						Core//PE/OE
PC505EC	MICRO-CONTROLLERS AND INTERFACING						Core
Pre-requisites	Contact Hours Per Week				CIE	SEE	Credits
Computer Organization, Micro Processors	L	T	D	P			
	3	-	-	-	30	45	3

Course Objectives:

The course is taught with the objectives of enabling the student to:

1. Discuss 8051 Basic architecture and programming.
2. Discuss Timers, serial communication and interrupts of 8051.
3. Discuss ARM architecture and Programming.
4. Discuss Real time Interfacing and Programming.

Course Outcomes:

On completion of this course, the student will be able to:

1. To gain a comprehensive understanding of the 8051 microcontroller architecture and develop practical skills in programming.
2. Understand timers, serial communication, and interrupts in embedded systems, along with practical skills in programming these features on the 8051 microcontroller.
3. Understand RISC based ARM architecture.
4. Develop programs for basic problem solving.
5. Develop real time interfacing using 8051 and ARM

UNIT–I

8051 Architecture and Programming:

Evolution of Microprocessors and Microcontrollers, Programming model of 8051, Register Organization, Flag Register, Pin configuration, Memory Organization: ROM and RAM, Register Bank, Addressing modes and Instruction Set. Assembly Language Programming and C programming. Internal structure of Ports and alternate functions of Ports, 8051 I/O programming, bit manipulation programs using I/O ports

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UNIT–II

Timer, Serial communication and Interrupt programming:

Concept of Timer, Programming model of Timers of 8051, Programming timers, Basics of Serial Communication, Serial communication standards, Programming model of UART of 8051, Concept of Interrupts, 8051 Interrupts, Programming Interrupts.

UNIT–III

ARM-CORTEX-M: General Description, Features, Cortex M Architecture, Block Diagram, Registers, Reset, Memory, Operating Modes, Introduction to Input /Output

UNIT-IV

ARM Cortex-M Instruction Sets and Programming: The Software Development Process, ARM Cortex-M Assembly Language Syntax, Addressing Modes, Operands, Memory Access Instructions, Logical, Shift, Arithmetic Operations, Stack, Functions and Control Flow, Assembler Directives.

UNIT – V

Interfacing with 8051: D/A converter, A/D converter, multiplexed key board, multiplexed seven segment display, LCD interfacing, stepper Motor interfacing,

Interfacing with ARM Controller

I/O Port Programming and Direction Register, Switch inputs and LED outputs, PLL, Sys Tick Timer, PWM, Communication Interfaces.

Suggested Reading:

1. Mohammad Ali Mazidi, Rolin D McKinley, Janice G Mazidi, The 8051 Microcontroller and Embedded Systems, Second Edition, Prentice Hall
2. Raj Kamal, Embedded Systems – Architecture, Programming and Design, 2nd Edition, TMH, 2008
3. Jonathan w valvano, introduction to arm®cortex-M Microcontrollers volume 1, fifth edition june 2014
4. Dr. Yifeng Zhu , Embedded Systems with ARM Cortex-M Microcontrollers in Assembly Language and C
5. Kenneth J Ayala, The 8051 Micro Controller: Architecture, Programming and Applications

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Course Code	Course Title						Core//PE/OE
PC506EC	COMPUTER ARCHITECTURE AND ORGANIZATION						Core
Pre-requisites	Contact Hours Per Week				CIE	SEE	Credits
	L	T	D	P			
-	3	-	-	-	30	45	3

Course Objectives:

1. The course is taught with the objectives of enabling the student to:
2. To familiarize with Central Processing Unit (CPU) concepts.
3. To understand register, architecture, addressing modes and instruction set of Intel.
4. To design data path and control units of Central Processing Unit (CPU).
5. To know IO processor and cache memory organization.
6. To understand CPU performance enhancement strategies.

Course Outcomes:

1. On completion of this course, the student will be able to :
2. Design Arithmetic and Logic Unit for the given specifications.
3. Demonstrate data path and control unit realizations of CPU.
4. Analyze cache memory and IO organizations.
5. Incorporate pipeline concept in a Central Processing Unit (CPU).
6. Develop programs of Intel Microprocessor.
7.

UNIT– I

CPU Organization: Register Transfer Language (RTL), Common bus structure, Arithmetic, Logic and Shift Unit using multiplexer, Design of Basic CPU, HDL Realization of Basic CPU.

UNIT– II

Data Path Design: Fixed-Point Arithmetic: Addition, Subtraction, Booth"s algorithms for multiplication, Array Multiplier and Wallace tree multiplication, Division - Restoring and Non-restoring algorithms, Overhead in floating point arithmetic, HDL descriptions of Fixed-Point arithmetic.

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UNIT- III

Control Design: Basic concepts, Hardwired Control unit design approach: classical and one-hot methods, Micro-programmed Control unit approach: basic concept, micro-program sequencer, Design examples: control unit designs for GCD processor, DMA controller and CPU control unit.

UNIT- IV:

Memory Organization: Memory Organization, Memory hierarchy, Main memory: RAM, ROM, DRAM, Multi

System Organization: System Organization: communication methods, IO and system control: Programmed IO, DMA and interrupts and Input

UNIT- V

Performance Enhancement Strategies: Reduced Instruction Set Computer (RISC): characteristics and architecture, Parallel processing: Pipeline – Arithmetic and Instruction, Pipeline Conflicts.

The 8086 Microprocessor Family- Overview, 8086 architectures, Flag Register, Segmented memory, Maximum and Minimum mode of operation, Addressing modes, Memory read and Write bus cycles, Instruction Set: data transfer, arithmetic, logical, program Jumps and String instructions, Addressing Modes and Practicing Programs of 8086.

Suggested Reading:

1. Morris Mano M, *Computer System Architecture*, 3rd edition, Prentice Hall India, 2007.

2. John P. Hayes, *Computer Architecture and Organization*, 3rd edition, McGraw Hill, 1998.

3. Douglas V.Hall, "Microprocessors and Interfacing Programming and Hardware", 2nd Edition, Tata McGraw- Hill publishing company Limited, New Delhi, 1994.

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Course Code	Course Title						Core//PE/OE
PE511CS	ARTIFICIAL INTELLIGENCE AND MACHINE LEARNING						PE-II
Pre-requisites	Contact Hours Per Week				CIE	SEE	Credits
	L	T	D	P			
-	3	-	-	-	30	45	3

Course Objectives:

The course is taught with the objectives of enabling the student to:

1. Understand different types of Intelligent agents and Various search algorithms.
2. Learn game-playing and CSP techniques.
3. Learn Knowledge Representation, Reasoning, and Planning.
4. Acquire knowledge of Probabilistic Reasoning.
5. Understand the concepts of learning and its application.

Course Outcomes:

On completion of this course, the student will be able to:

1. Apply various search algorithms in real time.
2. Apply game-playing and CSP techniques.
3. Perform Knowledge Representation, Logical Reasoning and Planning.
4. Perform Probabilistic Reasoning.
5. Apply different types of learning in applications like NLP, robotics

UNIT–I

Introduction to AI: Introduction, history, intelligent systems, foundations of AI, applications, development of AI languages, current trends.

UNIT–II

Artificial Neural Networks: Biological neurons and artificial neurons. Model of an ANN. Activation functions used in ANNs. Typical classes of network architectures. Mathematical Foundations and Learning mechanisms, Feed forward ANN: Structures of Multi-layer feed forward networks. Back propagation algorithm. Back propagation – training and convergence. Functional approximation with back propagation.

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UNIT-III

Supervised Machine Learning: Basics of linear regression, its assumptions, limitations and industry applications. Least square based and Gradient Descent Based Regression, Multiple linear regression, Polynomial regression, Logistic regression.

UNIT-IV

Unsupervised Machine Learning: Different clustering methods (Distance, Density, Hierarchical) , Iterative distance-based clustering; K-Means Clustering Algorithm and Image Quantization, basics of Principal Component Analysis

UNIT – V

Introduction to Deep learning: Analyze the key computations underlying deep learning, Convolutional Neural Network, Building blocks of CNN- Convolutional layers, Pooling layers Dense layers.

Suggested Reading:

1 Stuart Russell and Peter Norvig, "Artificial Intelligence: A Modern Approach", Third Edition, Pearson 2016.

2 Elaine Rich, Kevin Knight, Shivashankar B. Nair, Artificial Intelligence", McGraw-Hill, Third Edition, 2009.

3 Dan W. Patterson, "Introduction to Al and ES", Pearson, 2007.

4 Patrick H. Winston, "Artificial Intelligence", Third Edition, Pearson Education, 2006

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Course Code	Course Title						Core//PE/OE
PE512EC	DIGITAL IMAGE PROCESSING						PE-II
Pre-requisites	Contact Hours Per Week				CIE	SEE	Credits
	L	T	D	P			
Digital Signal Processing	3	-	-	-	30	45	3

Course Objectives:

The course is taught with the objectives of enabling the student to:

1. Understand the image formation and its digital representation.
2. Learn digital image fundamentals. Be exposed to simple image processing techniques.
3. Learn representation of images in frequency domain and enhancement techniques.
4. Be familiar with image compression and segmentation techniques. Learn to represent image in form of features.
5. Solve the problems related to image compression and learn the basics of video.

Course Outcomes:

On completion of this course, the student will be able to :

1. Understand how images are formed, sampled and quantized.
2. Apply various transforms like Fourier, DCT, Haar, DWT and Hadamard Transform to different applications.
3. Apply image enhancement techniques for practical applications.
4. Implement the image restoration techniques.
5. Implement image compression techniques by removing the redundancy

UNIT–I

Digital Image Fundamentals: Image sensing, acquisition, Image formation model, sampling and Quantization, Basic relationships between pixels; neighbors of a pixel, adjacency, connectivity, regions and boundaries. Image formation in the eye, its capabilities for brightness adaptation and discrimination. Categorization of images according to their source. Gamma ray imaging, x-ray imaging, imaging in the Ultra Violet band, visible and infrared bands, Microwave band and Radio band.

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UNIT-II

Image Transforms: 2D Fourier transform, Properties of 2D Fourier transform, Walsh, Hadamard, Slant, Haar, Discrete cosine transform and Hotelling transform. Color Image Processing: Color Fundamentals, Color Models, Pseudo color Image Processing.

UNIT-III

Image Enhancement: Spatial domain techniques: Contrast stretching, histogram equalization and histogram specification method, Neighborhood averaging and adaptive Median filter. Frequency domain methods: Ideal Low pass, Butterworth and Gaussian Low pass filters. Ideal High pass, Butterworth and Gaussian High pass filters. Homomorphic filtering.

UNIT-IV

Image Restoration: Mathematical expression for degraded image, estimation of degradation functions: Image observation, experimentation and modeling. Inverse filter Wiener filter, Geometric transformation, periodic noise reduction method.

Image Segmentation and Compression: Detection of discontinuities, point detection methods, line detection. Edge detection methods: Gradient operation, Laplacian, Prewitt, Sobel, Laplacian of a Gaussian and Canny edge detectors.

UNIT – V

Image compression: Functional block diagram of a general image compression system and description of each unit, various types of redundancies, coding redundancy, psycho visual redundancy spatial and temporal redundancy, Huffman coding.

Video Sampling: Analog video, Digital Video, Time-varying Image formation models, 3D motion models, Geometric image formation, Photometric image formation, Sampling of video Signals

Suggested Reading:

1. Rafael C. Gonzales, Richard E. Woods, "*Digital Image Processing*", Third Edition, Pearson Education, 2010.
2. Anil K Jain, "*Fundamentals of Digital Image Processing*", Prentice-Hall of India Private Limited, New Delhi, 1995
3. . Milan Sonka, Vaclav Havel and Roger Boyle, "*Digital Image Processing and Computer vision*", Cengage Learning India Pvt. Limited, 2008.
4. M. Tekalp, "Digital Video Processing", Prentice-Hall International, Second Edition,2015
5. ALAN C BOVIK, —Hand Book of Image and Video Processing, 2nd Edition, Elsevier Academic Press, 2005

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Course Code	Course Title						Core//PE/OE
PE513CS	OBJECT ORIENTED PROGRAMMING USING C++						PE-II
Pre-requisites	Contact Hours Per Week				CIE	SEE	Credits
	L	T	D	P			
-	3	-	-	-	30	45	3

Course Objectives:

The course is taught with the objectives that the student is:

1. Introduced to Object Oriented Programming concepts using the C++ language.
2. Introduced to the principles of data abstraction, inheritance and polymorphism.
3. Introduced to the principles of virtual functions and polymorphism.
4. Introduced with handling formatted I/O and unformatted I/O.
5. Introduced to handle exceptions.

Course Outcomes:

On completion of this course, the student will be able to:

1. Able to develop programs with reusability.
2. Understand different types of constructors and initialization of objects.
3. Handle exceptions in programming.
4. Handle formatted and unformatted I/O.
5. Develop applications for a range of problems using object-oriented programming techniques

UNIT–I

Basic Concepts of OOP: Benefits of OOP, Object Oriented Languages, Features of OOP. How OOP Differ from POP. Applications of OOP, A Simple C++ Program, Structure of C++ Program. Keywords, Identifiers and Constants, Basic Data Types, User Defined Data Types, Derived Data Types, Dynamic Initialization of Variables, Reference Variables, Operators in C++, Scope Resolution Operator, Member Dereferencing Operators, Memory Management Operators

UNIT–II

Functions, Classes and Objects: Introduction of Classes, Specifying a Class, Defining a Member Functions, A C++ Program with Class Access Specifies, Inline functions, Nesting of Member Functions, Memory Allocation for Objects, Static Data Members, Static Member

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Functions, Arrays of Objects, Objects as Function Arguments, Default Arguments, Constant Arguments, Function Overloading, Friend Functions.

UNIT–III

Constructors, Destructors, Inheritance: Introduction, Constructors, Parameterized Constructors, Multiple Constructors in a Class, Constructors with Default Arguments, Dynamic initialization of Objects, Copy Constructors, Dynamic Constructors, Destructors, Introduction to inheritance, Defining Derived Classes, Single Inheritance, Multiple Inheritance, Multi-Level Inheritance, Hierarchical Inheritance, Hybrid Inheritance, Abstract Classes, Constructors in Derived Classes, Containership, Operator overloading, Rules for Operator overloading, overloading of binary and unary operators.

UNIT-IV

Pointers, Virtual Functions and Polymorphism: Introduction, Memory Management, new Operator and delete Operator, Pointers to Objects, this Pointer, Pointers to Derived Classes, Polymorphism, compile time polymorphism, Run time polymorphism, Virtual Functions, Pure Virtual Functions, Virtual Base Classes, Virtual Destructors.

UNIT – V

Templates and Exception handling: Introduction, Class Templates, Class Templates with Multiple Parameters, Function Templates, Function Templates with Multiple Parameters, Member Function Templates. Basics of Exception Handling, Types of exceptions, Exception Handing Mechanism, Throwing and Catching Mechanism, Re-throwing an Exception, Specifying Exceptions.

Suggested Reading:

1. Walter Savitch, *"Problem Solving with C++"*, 6th Edition, Pearson Education Publishing.
2. SB Lippman, J Lajoie, *"C++ Primer"*, 3rd Edition, AW Publishing Company, 2007.
3. Paul Dietel, Harvey Dietel, *"C How to Program"*, 6th Edition, PHI, 2010.
4. BjarneStroustrup, *"The C++ Programming Language"*, 3rd Edition, Pearson Education.
5. Ashok N.Kamthane, *"Programming in C++"* 2nd Edition, Pearson Education Publishing.

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Course Code	Course Title						Core//PE/OE
PC551EC	DIGITAL COMMUNICATION LAB						Core
Pre-requisites	Contact Hours Per Week				CIE	SEE	Credits
Analog and Digital Communications	L	T	D	P			
	-	-	-	2	20	30	1

Course Objectives:

1. To perform Analog modulation and demodulation techniques and measure modulation index.
2. To perform experiments on Radio Receivers to measure their performance parameters.
3. To perform Pulse digital modulation and demodulation techniques and understand.
4. To perform carrier modulation techniques.

Course Outcomes: Student will be

1. Able to acquire knowledge of performing modulation and demodulation and analyze the effects of various parameters on the process.
2. Able to acquire in-depth understanding of pulse digital modulation techniques.
3. Able to acquire skill to perform carrier modulation schemes using MATLAB.

Experiments

1. PCM Generation And Detection
2. Differential and adaptive delta modulation.
3. Delta Modulation
4. Frequency Shift Keying: Generation And Detection
5. Phase Shift Keying: Generation And Detection
6. Amplitude Shift Keying: Generation And Detection
7. Concept of aliasing using MATLAB
8. Sampling and quantization using MAT LAB
9. ASK modulation and Demodulation using MATLAB
10. FSK modulation and Demodulation using MATLAB

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11. PSK modulation and Demodulation using MATLAB
12. QPSK modulation and Demodulation using MATLAB

Suggested Reading:

1 Simon Haykin. *"Communication Systems"*, 4th Edition, John Wiley &sons.inc, 2000.

2 George Kennedy, Bernard Davis, *"Electronic Communication Systems"*, 4th Edition, Tata McGraw-Hill publishing company Limited, New Delhi, 1993.

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Course Code	Course Title						Core//PE/OE
PC552EC	DIGITAL SYSTEM DESIGN LAB						Core
Pre-requisites	Contact Hours Per Week				CIE	SEE	Credits
Switching theory and logic design	L	T	D	P			
	-	-	-	2	20	30	1

Course Objectives:

The course is taught with the objectives of enabling the student to:

1. To understand the operation of basic combinational building blocks
2. Understand the operation of display devices and perform code conversions
3. Understand the operation of all Flip flops
4. Design combinational and sequential circuits for given applications
5. Understand and design counters and registers using basic building blocks

Course Outcomes:

On completion of this course, the student will be able to :

1. Use all basic building blocks to design any combinational functions
2. Configure and use display devices
3. Use all flip flops in sequential design and convert flip flops from one form to another
4. Use and configure IC counters as per the given specification
5. Design resisters and use them as per the application

Experiments

1. Verification of all basic gates using universal gates
2. TTL combinational gate applications: Design and verify
   - 4-bit Binary to Grey code converter
   - AOI gate using 7400IC
   - Full adder and Full Subtractor
   - Full adder using 4:1 Mux
3. Verify the operation of BCD- &-segment Decoder (7447) using 7-segment LED display
4. Verify the function of all flip flops realized using basic gates (SR, JK, T and D)
5. Convert JK flip flop into T and D Flip flops and verify the design (use basic gates)
6. Design and verify a 3-bit up/down counter

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7. Configure IC 7483 as BCD adder and verify the design
8. Verify the operation of decade counter using IC 7490, configure it as Mod-N (N<9) counter and verify the operation
9. Configure IC 7492 as divide by 3 and 6 counter and verify
10. Construct a 4-bit shift register (SISO) using IC 7476 and other logic gates and verify the operation
11. Design and verify the following using Verilog HDL
    - 8:1 encoder using structural modeling
    - BCD – 7-segment decoder using conditional constructs
    - 8:1 Encoder using behavioral constructs
    - ALU using Case Construct
    - 3-bit Binary counter using Loop statements

SUGGESTED READING:

1. R.P.Jain, "Modern Digital Electronics", Tata McGraw Hill, 4th Edition, 2009
2. M.Morris Mano, Michael D. Ciletti, "Digital Design", Pearson, 4th Edition, 2012
3. Ming-Bo Lin, "Digital System Design and Practices Using Verilog HDL and FPGAs", Wiley India Pvt. Ltd., 2012

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Propose Scheme of Instruction, Evaluation

For Four year B.Tech program In Electrical & Communication Engineering

With effect from Academic Year 2025-26

DEPARTMENT OF ELECTRICAL AND COMMUNICATION ENGINEERING

UNIVERSITY COLLEGE OF ENGINEEIRNG & TECHNOLOGY

MAHATMA GANDHI UNIVERSITY

Nalgonda - 508001, TS, INDIA

SCHEME OF INSTRUCTION AND EVALUATION
B. TECH (ELECTRONICS AND COMMUNICATION ENGINEERING)

(With effect from AY: 2025-26)

VI-SEMESTER

SNo	Code	Course Title	Scheme of Instruction			Contact Hrs/Wk	Scheme of Evaluation			Credits
			L	T	P		Hrs	CIE	SEE	
Theory										
1	PC601EC	Digital Signal Processing	3	-	-	3	3	30	45	3
2	PC602 EC	Data Communications and Computer Networks	3	-	-	3	3	30	45	3
3	PC603 EC	Embedded System Design	3	-	-	3	3	30	45	3
4	PC604EC	VLSI Design	3	-	-	3	3	30	45	3
5	**Professional Elective-III**		3	-	-	3	3	30	45	3
	PE611CS	Deep Learning								
	PE612EC	Satellite Communication and Applications								
	PE613EC	Radar Systems								
	PE614EC	Optical Communications								
6	OE######	Open Elective-I	3	-	-	3	3	30	45	3
Practicals										
7	PC651EC	Micro Controller Lab	-	-	2	2	3	20	30	1
8	PC652EC	Digital Signal Processing Lab	-	-	2	2	3	20	30	1
9	PC653EC	Electronic Design Automation Lab	-	-	2	2	3	20	30	1
10	PC654EC	Mini-Project	-	-	6	6	-	50	-	3
		Total	**18**	**-**	**12**	**30**	**27**	**290**	**400**	**24**

*Students have to undergo summer internship of 6 Weeks duration at the end of semester VI and Evaluation will be done in VII - Semester

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Open Elective-I

1	OE601CE	Disaster Management
2	OE602CE	Road Safety Engineering
3	OE601CS	Python Programming
4	OE602CS	Cyber Security
5	OE603CS	Deep Learning
6	OE601EC	Verilog HDL
7	OE602EC	Principles of Electronic Communication Systems
8	OE601EE	Applications of Electrical Energy
9	OE602EE	Electrical Safety Management
10	OE601ME	3D Printing Technology
11	OE602ME	Finite Element Methods
12	OE603ME	Fundamentals of Robotics
13	OE601MB	Principles of Management

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Course Code	Course Title						Core//PE/OE
PC601EC	DIGITAL SIGNAL PROCESSING						Core
Pre-requisites	Contact Hours Per Week				CIE	SEE	Credits
Switching theory and logic design	L	T	D	P			
	-	-	-	2	30	45	3

Course Objectives:

1. The course is taught with the objectives of enabling the student to:
2. To study about discrete time systems and to learn about the DFT and FFT algorithms.
3. To study the design techniques for FIR and IIR digital filters
4. To study the finite word length effects in signal processing
5. To understand Multi rate signal processing
6. To study the architecture of TMS processor

Course Outcomes:

1. On completion of this course, the student will be able to :
2. Find DFT of a given signal through Fast Fourier Transform techniques.
3. Design FIR and IIR type digital filters.
4. Identify filter structures and evaluate the coefficient quantization effects.
5. Understand sample rate conversion techniques.
6. Compare the architectures of DSP and General Purpose Processors.

UNIT– I

Introduction: Concept of frequency in continuous and discrete time signals, DFT and its properties, linear convolution, circular convolution. Computational complexity of direct Computation of DFT, Fast Fourier Transform, DIT and DIF, FFT algorithms for RADIX-2 case, in-place computation, Bit reversal, Finite word length effects in FFT algorithms, Use of FFT in Linear Filtering

UNIT – II

FIR Filters: FIR digital filter design techniques. Properties of FIR digital filters, design of FIR filters using windows and frequency sampling techniques, linear phase characteristics. Realization diagrams for IIR and FIR filters, finite word length effects

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UNIT– III

IIR Filters: Analog filter design – Butterworth and Chebyshev approximations, IIR digital filter design techniques, impulse invariant technique. Bilinear transform technique. Comparison of FIR and IIR filters, frequency transformations.

UNIT– IV

Multirate signal processing: Introduction, decimation by a factor D, interpolation by a factor I, sampling rate conversion by a rational factor I/D, design of practical sampling rate converter, S/W implementation of sampling rate converter, application of Multirate signal processing.

UNIT–V

DSP Processors: Introduction to Fixed point Digital Signal Processors, TMS 320C54XX processor-architecture, addressing modes, instruction set, Assembly programming, programming issues, Applications of DSP processors.

Suggested Reading:

1. John G.Proakis and Dimitris G. Manolakis, *"Digital Signal Processing principles, Algorithms and Applications"*, 3rd Edition, Prentice-Hall of India Private Limited, New Delhi, 1997.
2. Alan V. Oppenheim and Ronald W. Schafer, "*Discrete Time Signal Processing*", 3rd edition, Prentice Hall, Upper Saddle River, NJ,2010.
3. Sanjit K. Mitra, "*Digital Signal Processing: A Computer-Based Approach*", 4/e, McGraw-Hill, New York,2011.
4. Avatar sing and S.Srinivasan, *"Digital Signal Processing implementation using DSP Microprocessors with Examples from TMS320C54XX"*, Thomson Books Icole, 2004.

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Course Code	Course Title						Core//PE/OE
PC602EC	DATA COMMUNICATIONS AND COMPUTER NETWORKS						Core
Pre-requisites	Contact Hours Per Week				CIE	SEE	Credits
Computer Organization	L	T	D	P			
	3	-	-	-	30	45	3

Course Objectives:

The course is taught with the objectives of enabling the student to:

1. To provide a conceptual foundation for the study of data communications using the *Open* Systems Interconnect (OSI) model for layer architecture,
2. To study the principles of network protocols and Internet working,
3. To understand the Network security and Internet applications,
4. To understand the concepts of switched communication networks,
5. To understand the performance of data link layer protocols for error and flow control and network security.

Course Outcomes:

On completion of this course, the student will be able to:

1. Understand the working of various network topologies, circuit and packet switching,
2. Comprehend the role of data link layers and significance of MAC protocols,
3. Understand the networking protocols and Internet protocols,
4. Understand the transport layer working with TCP, UDP and ATM protocols,
5. Comprehend the functionality of application layer and importance of network security

UNIT–I

Data communication: A Communication Model, The Need for Protocol Architecture and Standardization, Network Types: LAN, WAN, MAN. Network Topologies: Bus, Star, Ring, Hybrid. Line configurations. Reference Models: OSI, TCP/IP.

Circuit switching: Circuit Switching Principles and concepts.

Packet switching: Virtual circuit and Datagram subnets, X.25.

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UNIT–II

Data Link Layer: Need for Data Link Control, Design Issues, Framing, Error Detection and Correction, Flow control Protocols: Stop and Wait, Sliding Window, ARQ Protocols, HDLC. **MAC Sub Layer:** Multiple Access Protocols: ALOHA, CSMA, Wireless LAN, IEEE 802.2, 802.3, 802.4, 802.11, 802.15, 802.16 standards, Bridges and Routers.

UNIT–III

Network Layer: Network layer Services, Routing algorithms: Shortest Path Routing, Flooding, Hierarchical routing, Broadcast, Multicast, Distance Vector Routing, and Congestion Control Algorithms

Internet Working: The Network Layer in Internet: IPV4, IPV6, Comparison of IPV4 and IPV6, IP Addressing, ATM Networks

UNIT-IV

Transport Layer: Transport Services, Elements of Transport Layer, Connection management, TCP and UDP protocols, ATM AAL Layer Protocol.

UNIT – V

Application Layer: Domain Name System, SNMP, Electronic Mail, World Wide Web. **Network Security:** Cryptography Symmetric Key and Public Key algorithms, Digital Signatures, Authentication Protocols.

Suggested Reading:

1. Andrew S Tanenbaum, "Computer Networks," 5/e, Pearson Education, 2011.
2. Behrouz A. Forouzan, "Data Communication and Networking,"3/e, TMH, 2008.
3. William Stallings, "Data and Computer Communications," 8/e, PHI, 2004.
4. Douglas E Comer, "Computer Networks and Internet", Pearson Education Asia, 2000.
5. Prakash C. Gupta, "Data Communications and Computer Networks", PHI learning,2013

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Course Code	Course Title						Core//PE/OE
PC603EC	EMBEDDED SYSTEM DESIGN						Core
Pre-requisites	Contact Hours Per Week				CIE	SEE	Credits
	L	T	D	P			
-	3	-	-	-	30	45	3

urse Objectives:

: course is taught with the objectives of enabling the student to:

1. To understand the processor selection criteria for Embedded System Design.
2. To provide a clear understanding of role of firmware, operating systems in correlation with hardware systems.
3. To gain the knowledge of tool chain for embedded systems.
4. To understand the importance of RTOS in building real time systems

urse Outcomes:

n completion of this course, the student will be able to:

1. Design an embedded system.
2. Distinguish between RISC and CISC
3. Design procedure of embedded firm ware
4. Use Embedded Software Development Tools for Designing Embedded System applications
5. Apply their understanding in building real time systems

UNIT–I

Introduction to Embedded Systems: Embedded systems Vs General computing systems, History of Embedded systems, classification, Characteristics and quality attributes of Embedded Systems Challenges in Embedded System Design, Application and Domain specific Embedded Systems.

UNIT–II

Embedded firmware and Design and Development: Embedded Firmware Design Approaches and Development languages and Programming in Embedded C

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UNIT–III

Embedded Software Development Tools: Host and Target Machines, Cross Compilers, Cross Assemblers, Tool Chains, Linkers/Locators for Embedded Software, Address Resolution, Locator Maps. Getting Embedded Software Into Target System: PROM programmer, ROM emulator, In Circuit- Emulators, Monitors, Testing on Your Host Machine - Interaction bet Simulators, Logic Analysers.

UNIT-IV

Introduction to Real Time Operating Systems: Tasks and task states, tasks and Data, Semaphores and shared data. Operating system services: Message queues, mailboxes and pipes, timer functions, events, memory management, Interrupt routines in an RTOS environment.

UNIT – V

TASK COMMUNICATION: Shared Memory, Message Passing, Remote Procedure Call and Sockets, Task Synchronization: Task Communication/Synchronization Issues, Task Synchronization Techniques, Device Drivers, How to Choose an RTOS.

Suggested Reading:

1. Shibu KV, Introduction to Embedded System, Mc-Graw Hill, 2010.
2. Raj Kamal, Embedded Systems Architecture, Programming and Design, 2nd Ed., McGraw Hill, 2010
3. An Embedded Software Primer - David E. Simon, Pearson Education.
4. Jean.J.Labrosse, *MicroC/OS-II*, Taylor & Francis, 2002

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Course Code	Course Title						Core/PE/OE
PC604EC	VLSI DESIGN						Core
Pre-requisites	Contact Hours Per Week				CIE	SEE	Credits
ED, STLD and DSPBBL	L	T	D	P			
	3	-	-	-	30	45	3

Course Objectives:

The course is taught with the objectives of enabling the student to:

1. To provide a perspective on Digital Design in the Deep Sub-micron Technology
2. To focus on CMOS and Bi CMOS Short-channel Transistor Models
3. To Study CMOS Inverter elaborately
4. To explore static and dynamic implementations of combinational and sequential circuit designs
5. Introduce Testability of VLSI circuits.

Course Outcomes:

On completion of this course, the student will be able to:

1. Have an understanding of the Fabrication processes and the comparison between different state-of-the-art CMOS technologies
2. Acquire the knowledge in understanding CMOS Inverter characteristics, Illustrate circuit diagrams, stick diagrams and layouts
3. Design and analyze various Combinational Logic circuits in different models
4. Design and analyze various Arithmetic Blocks and Memory structures
5. Understand various fault models and testing methods

UNIT–I

Design Abstraction in Digital circuits, Fabrication process flow of NMOS and PMOS transistors, Overview of CMOS and BiCMOS technologies, MOSFET Transistor under static conditions, channel Length Modulation, Velocity Saturation, Sub-threshold Condition, Threshold variations, MOS structure Capacitance, CMOS Latch up, Technology scaling

UNIT–II

Digital CMOS Design: Static CMOS inverter-switching threshold, Noise margins, Voltage Transfer Characteristics, CMOS inverter Dynamic behavior- computing capacitance and propagation delay, Static and Dynamic Power Consumption.

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Complementary CMOS, Ratioed Logic, Pass Transistor Logic, Full Adder and carry save multiplier design Considerations, Dynamic CMOS design -basic principle, Signal integrity Issues in Dynamic Design, easeading dynamic gates. Designing sequential logic circuit- Bi stability Principle, Multiplexer based latch, Dynamic latches and registers.

UNIT–III

Analog CMOS design: Significance of analog integrated circuits, Suitability of CMOS for analog IC design, , biasing styles, single stage amplifier with resistive load, single stage amplifier with diode connected load, CS, CD, CG amplifiers, current sources and sinks, limitations of single stage amplifier, gain boosting techniques, current mirror principles, introduction to differential amplifier

UNIT-IV

Designing Memory and Array architectures: Memory classification, Architecture and building blocks, Memory Core-Rom, Non volatile RWM, RAM-Static RAM, Dynamic RAM, Memory Peripheral Circuitry-Address decoders-Row decoders-static and dynamic, column and block decoders, sense amplifiers-differential and single ended sensing, voltage references, Timing and control.

UNIT – V

Implementation of strategies for Digital ICs, Testing of VLSI circuits: VLSI Chip Yield, Test procedures; Design for Testability- Ad Hoc Testing, Scan Based testing, Boundary Scan Design, Built in Self-Test, Built-in logic block observer, Test Pattern Generator, Automatic Test Pattern Generation (ATPG)

Suggested Reading:

1. JAN.M. Rabaey, A. Chandrakasan and B. Nikholic, *"Digital Integrated Circuits – A Design Perspective"*, 2nd Edition, PHI, 2007.
2. David A Hodges, H. Jackson and R. A. Saleh, "*Analysis and Design of Digital Integrated Circuits in Deep Submicron Technology*", 3rd Edition, Tata McGraw Hill, 2007.
3. John. P. Uymera, "*Introduction to VLSI Circuits and system*", student edition, John Wiley and Sons, 2003
4. Douglas A. Pucknell, Kamran Eshraghian, " Basic VLSI design", 3rd Edition, PHI, 2003

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Course Code	Course Title						Core//PE/OE
PC611CS	DEEP LEARNING						PE-III
Pre-requisites	Contact Hours Per Week				CIE	SEE	Credits
	L	T	D	P			
-	3	-	-	-	30	45	3

Course Objectives:

The course is taught with the objectives of enabling the student to:

1. To understand complexity of Deep Learning algorithms and their limitations
2. To understand modern notions in data analysis oriented computing;
3. To apply Deep Learning algorithms in practical applications
4. To perform experiments in Deep Learning using real-world data.

Course Outcomes:

The student will be able to

1. Understand the concepts of Neural Networks, its main functions, operations and the execution pipeline implement.
2. Learn topics such as deep learning algorithms understand neural networks and traverse the layers of data abstraction Convolutional neural networks, recurrent neural networks, training deep networks and modifications.
3. Build deep learning models in PyTorch and interpret the results.

UNIT-I

Artificial Neural Networks: Introduction, Perceptron, XOR Gate ,Perceptron Training Rule, Activation Functions.

Linear Neural Networks: Linear Regression, Implementation of Linear Regression, Softmax Regression, The Image Classification Dataset , Implementation of Softmax Regression

UNIT-II

Multilayer Perceptrons:

Underfitting and Overfitting, Weight Decay, Dropout, Forward Propagation, Backward Propagation, and Computational Graphs, Numerical Stability and Initialization,

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Considering the Environment, Predicting House Prices on Kaggle.

Optimization Algorithms: Optimization and Deep Learning, Convexity, Gradient Descent, Stochastic Gradient Descent, Mini batch Stochastic Gradient Descent, Momentum, Adagrad, RMS Prop, Ada delta, Adam, Learning Rate Scheduling.

UNIT-III

Introduction to Convolutional Neural Networks

Introduction to CNNs, Kernel filter, Principles behind CNNs, Multiple fillters,

Modern Convolutional Neural Networks

Deep Convolutional Neural Networks (AlexNet), Networks Using Blocks (VGG), Network in Network (NiN), Networks with Parallel Concatenations (GoogLeNet), Batch Normalization, Residual Networks (ResNet), Densely Connected Networks (DenseNet).

UNIT-IV

Recurrent Neural Networks: Sequence Models, Text Preprocessing, Language Models and the Dataset, Recurrent Neural Networks, Implementation of Recurrent Neural Networks from Scratch, Concise Implementation of Recurrent Neural Networks, Back propagation Through Time.

Modern Recurrent Neural Networks: Gated Recurrent Units (GRU), Long Short Term Memory (LST), Deep Recurrent Neural Networks, Bidirectional Recurrent Neural Networks, Machine Translation and the Dataset, Encoder-Decoder Architecture, Sequence to Sequence, Beam Search.

UNIT-V

Auto Encoders: Types of Auto Encoders and its applications

Generative Adversarial Networks: Generative Adversarial Network, Deep Convolutional Generative Adversarial Networks.

Suggested Reading:

1. Good fellow, I., Bengio, Y., and Courville, A., "*Deep Learning*", MIT Press, 2016.

2. Aston Zhang, Zachary C. Lipton, Mu Li, and Alexander J. Smola, "*Dive into Deep Learning*", 2020.

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Course Code	Course Title						Core//PE/OE
PE612EC	SATELLITE COMMUNICATION AND APPLICATIONS						PE-III
Pre-requisites	Contact Hours Per Week				CIE	SEE	Credits
	L	T	D	P			
-	3	-	-	-	30	45	3

Course Objectives: The course is taught with the objectives of enabling the student to:

1. To familiarize with basic concepts related to satellite Communication
2. To understand Sub-Systems of Satellites and Launches
3. To study about the Satellite signal propagation.
4. To know about the Satellite Navigation.
5. To understand about the Deep Space missions and applications of satellites

Course Outcomes: On completion of this course, the student will be able to :

1. Have knowledge about the Satellite communications Principles and Properties
2. Know about the Space craft subsystems, Launch vehicles and Satellite link.
3. Able to design the satellite signal propagation effects
4. Able to analyze the significance and operation of satellite navigation systems.
5. Able to understand the space missions and applications of Satellite Communication

UNIT-I

Origin of Satellite communications, A Brief History of Satellite Communication, Basic principles and properties of satellite communication. Earth segment, Space segment, Interpretation of Kepler's Laws, Space craft sub systems, Orbital Mechanics: The Equation of the Orbit, Describing the Orbit, Locating the Satellite in the Orbit, Orbital effects in communication system Performance: Doppler shift, Range variation, Eclipse and Sun-Transit Outage

UNIT-II

Equipment Reliability and Space Qualification: Space Qualification, Reliability, and Redundancy, Satellite launch and launch vehicles and Mechanics of Launching a Synchronous Satellite. Earth Stations – Types of Earth stations- large, medium and small. Satellite Link Design: Basic Transmission Theory, System Noise Temperature and G/T ratio, Noise Temperature, calculation of System Noise Temperature and Noise Figure

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UNIT-III

Propagation on Satellite-Earth paths: Attenuation, depolarization, atmospheric absorption, Tropospheric Multipath effects and Land and Sea Multipath, Multipath Effects in System Design, Faraday rotation in the Ionosphere, Ionospheric scintillations, Rain and ice effects. Concept of TDMA

UNIT-IV

Satellite Navigation Applications: Significance, Transit system, Global and Regional Satellite Navigation Systems-Operating Principle, Advantages, Limitations, Current Status and Applications, Remote Sensing Satellites

UNIT – V

Space debris-History,Sources of debris, Hazards, Tracking and measurements, Debris removal Methods. Mars Orbiter mission, Chandrayaan1, 2 and 3 missions, Aditya L1, Gaganyaan, NISAR. Satellites, Indian Satellite Launchers under development, Indian Geo platform of ISRO -Bhuvan, Space applications

Suggested Reading:

1. Wilbur L. Pitchand and Henri G. Suyderhoud, Robert A. Nelson, —Satellite Communication Systems Engineeringl, 2ndedn. 3rd Impression, Pearson Education.2008.
2. Timothy Pratt and Charles Nestian. W, —Satellite Communicationl, John Wiley and Sons, 1988.
3. Tri T. Ha, —Digital Satellite Communicationl, Tata McGraw- Hill, Special Indian Edition 2009
4. https://www.isro.gov.in

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Course Code	Course Title						Core/PE/OE
PE613EC	RADAR SYSTEMS						PE-III
Pre-requisites	Contact Hours Per Week				CIE	SEE	Credits
	L	T	D	P			
-	3	-	-	-	30	45	3

Course Objectives: The course is taught with the objectives of enabling the student to:

1. Familiarize with basic concepts of radar systems
2. Understand different Radar Systems
3. Know about Radar antennas
4. Know the propagation effects on a radar signal
5. Understand tracking radar principles

Course Outcomes: On completion of this course, the student will be able to :

1. Understand the components of a radar system
2. Understand the components of a radar system
3. Analyze the concept of MTI radar systems
4. Incorporate the effects of environment condition in a radar system
5. Apply appropriate mathematical and computer models relevant to radar systems to calculate system performance

UNIT–I

Radar Systems: Radar Block diagram and operation, Applications of Radar. Radar frequencies, Radar Range Equation, Radar Cross Section of target, Prediction of range performance, Minimum detectable signal, Receiver noise figure, Effective noise temperature, Signal to noise ratio, System losses, False alarm time and probability of false alarm, Integration of radar pulses, Pulse-repetition frequency and range ambiguities. Swerling's Models.

UNIT–II

CW and FMCW Radars: Doppler effects, CW Radar, FMCW Radar, Multiple frequency CW radar, Low noise front-ends, A-scope, B-scope, PPI Displays, and Duplexers

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UNIT-III

MTI and Pulse Doppler Radar: MTI radar, Delay line canceller, Multiple and staggered prf, Blind speeds, Limitations to MTI performance, MTI using range gated Doppler filters, Pulse Doppler radar, Non coherent radar. CFAR techniques in Radar Detection

UNIT-IV

Tracking Radar: Sequential Lobing, Conical scan, Monopulse - Amplitude comparison and Phase comparison methods, tracking in range and in Doppler, Acquisition, and Comparison of Trackers

UNIT – V

Search Radar: Track while scan radars, Search radar range equation, Search scans, Effect of surface reflection, Line of Sight (LOS), Propagation effects: Propagation over a plane earth, the round earth, Refraction, Anomalous propagation, Diffraction, Attenuation by atmospheric gases, Environmental noise

Suggested Reading:

1. Skolnik, Merrill I, —*Introduction to Radar Systems*l, MGH, third edn. 2001.
2. Barton. David K, —*Modern Radar System Analysis*l, Artech House, 1988.
3. Peebles PZ, —*Radar Principles*l, John – Willey, 2004

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Course Code	Course Title						Core//PE/OE
PE614EC	OPTICAL COMMUNICATIONS						PE-III
Pre-requisites	Contact Hours Per Week				CIE	SEE	Credits
Analog and Digital Communications	L	T	D	P			
	3	-	-	-	30	45	3

Course Objectives: The course is taught with the objectives of enabling the student to:

1. To become familiar with the fundamental concepts of Light, Basic laws of light, various types of Optical fibers, modes and configurations.
2. To acquaint with theoretical analysis of the Signal propagation and distortion during propagation of light in Optical Fibers.
3. To become familiar with Optical sources, Optical detectors and Optical amplifiers.
4. To understand the design principles of Digital and Analog links.
5. To know the operating principles of WDM and components for its realization.

Course Outcomes: On completion of this course, the student will be able to:

1. Able to apply Optical Laws to provide solutions to the problems of Optical Waveguides.
2. Able to deal with the Optical Communication System designs.
3. Able to carry out the calculations of various noise powers at Optical Receivers.
4. Able to design the Optical Link Power Budget and Rise Time Budget for the given applications.
5. Able to design the WDM systems with various system considerations.

UNIT – I

Overview of Optical Fiber Communications: The evolution of optical fiber systems, Elements of an Optical fiber transmission link, Optical fibers, Nature of light – basic optical laws and definitions – Modes and configurations, Mode of theory of circular waveguides, Single and multi- mode step index and graded index fibers.

UNIT – II

Signal degradation in Optical fibers: Attenuation, Signal distortion in optical waveguides, Mode coupling, and Design optimization of single mode fibers.

Optical sources: Semiconductors as optical sources and their fabrication, LED's and Laser diodes, Linearity of sources.

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UNIT – III

Photo detectors: Physical principles of PIN and APD, Photo detector noise, Detector response time, Avalanche multiplication noise, Comparisons of Photo detectors.

Optical receiver operation: Fundamental receiver operation, Digital receiver performance calculation. Preamplifiers types, Analog receivers.

UNIT – IV

Point-to-Point Optical links: System considerations, Link power budget, Rise time budget, Noise effects on system performance, Overview of analog links, Carrier noise ratio in analog systems.

UNIT – V

Optical Amplifiers & WDM: Introduction to optical amplifiers, Basic applications and types of optical amplifier, WDM concepts and Components, operational principles, passive components, Tunable sources and Tunable filters.

Suggested Reading:

1. Gerd Keiser, "Optical Fiber Communications", 3rd Edition, Tata McGraw- Hill publishing company Limited, New Delhi, 2000.
2. D.C.Agarwal, "Fiber Optic Communication", 2nd Edition, Wheeler publishing, New Delhi, 1993.
3. D. k. Mynbaev, L.L. Scheiner, "Fiber-Optic Communications Technology", Pearson education, New Delhi, 2006.

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Course Code	Course Title						Core/PE/OE
PC651EC	MICRO-CONTROLLERS LAB						Core
Pre-requisites	Contact Hours Per Week				CIE	SEE	Credits
	L	T	D	P			
Micro-Controllers and Interfacing	-	-	-	2	20	30	1

Course Objectives: The course is taught with the objectives of enabling the student to:

1. Discuss Basic 8051 Assembly Language Programming
2. Discuss Basic ARM Programming
3. Discuss Timer & Interrupt Programming
4. Discuss Real time Interfacing using 8051 and ARM

Course Outcomes: On completion of this course, the student will be able to :

1. To understand Keil IDE for simulating 8051 and ARM7
2. To write basic assembly language programs for arithmetic and logical operations using 8051
3. To program Timers, serial communication and Interrupts using 8051
4. To interface ADC,DAC, LED, Seven Segment display, Stepper motor using 8051 and ARM LPC2148
5. To Write programs for PWM, sensor interfacing using LPC 2148

Experiments

1. Programs for Basic Arithmetic and Logical Operations using 8051.
2. Program for Sorting, Searching an array using 8051.
3. Generating Square wave form using Timers in 8051.
4. Program for serial communication using on-chip UART 8051.
5. Design of a Digital Clock using Timers/ Counters in 8051.
6. Design an interface to connect LEDs to 8051 and write a program for blinking LEDs.
7. Design a Seven Segment Display interface using 8051and write a program to display various numbers.
8. Interface ADC to 8051 and interface DAC to 8051, and write programs to generate Triangle and square waves.
9. Interface Stepper Motor using 8051, program it to rotate in clockwise and anti-clockwise directions.
10. Interface DIP Switches and LEDs to ARM, program to blink LEDs on switch press.
11. Design an interface to connect 16x2 LCD using ARMS

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12. Interface and rotate DC Motor using ARM.
13. Interfacing RTC Using I2C in ARM.
14. Using on chip Timers/Counters for PWM Generation using ARM

Suggested Reading:

Mohammad Ali Mazidi, Rolin D McKinley, Janice G Mazidi, The 8051 Microcontroller and Embedded Systems, Second Edition, Prentice Hall

Kenneth J Ayala, The 8051 Micro Controller: Architecture, Programming and Applications

Jonathan W. Valvano, " Introduction to ARM CORTEX-M Microcontrollers", Volume 1, fifth Edition, June 2024

Dr. Yifeng Zhu, "Embedded Systems with ARM Cortex-M micro controllers in Assembly Language ad C".

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Course Code	Course Title						Core//PE/OE
PC652EC	DIGITAL SIGNAL PROCESSING LAB						Core
Pre-requisites	Contact Hours Per Week				CIE	SEE	Credits
Micro-Controllers and Interfacing	L	T	D	P			
	-	-	-	2	20	30	1

Course Objectives:

1. The course is taught with the objectives of enabling the student:
2. To understand the concept of basic signals and to generate them using MATLAB.
3. To understand the concept of N-point FFT algorithm.
4. To understand the concept of analog and digital filters and simulation using MATLAB.
5. To study the architecture of TMS320 C54x.
6. To understand the concept of Linear Convolution and simulate it using CCSTUDIO

Course Outcomes:

1. On completion of this course, the student will be able to :
2. Examine the frequency response and impulse response of discrete-time LTI systems
3. Interpret discrete-time signals using DFT
4. Apply FFT algorithms for various signal processing operations.
5. Analyze IIR and FIR digital filters
6. Design IIR and FIR digital filters for real time DSP applications

Experiments

1. a). Generation of basic signals based on recursive difference equations.
   b). Operations on Basic sequences
2. a). Linear and Circular Convolutions in time domain and frequency domain
   b). Determination of autocorrelation and Power Spectrum of a given signal(s)
3. a). Fast Fourier Transform – DIT and DIF algorithm
   b). Spectrum analysis using DFT
4. a). Generation of windows – Rectangular, Hamming and Hanning window
   b). Design of LPF, HPF, BPF and BSF using windowing technique
5. a). Design of Butterworth Filter using Impulse Invariant and Bilinear transformation
   b). Design of Chebyshev Filter using Impulse Invariant and Bilinear transformation
6. a). Implementation of Decimation and Interpolation Process.
   b). Implementation of I/D sampling rate converters
7. a). Study of TMS320C54X DSP processor
   b).Arithmetic operation using TMS320C54XX
8. MAC operation using various addressing modes.

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a). FFT Implementation
b). Waveform Generation – Sine wave and Square wave
2. a). Perform linear convolution of given sequences/ signals
b). Perform circular convolution of given sequences/ signals
3. Implementation of FIR filter on DSP processor
4. Implementation of IIR filters on DSP processor.

ggested Reading:

1. John G Proakis. Vinay K. Ingle, "Digital Signal Processing Using MATLAB", Third Edition. © 2012 by Cengage Learning
2. Sanjit K. Mitra. "Digital Signal Processing: A Computer - Based Approach" second edition.McGrawHill
3. Preetham Kumar, "Digital Signal Processing Laboratory", © 2005 by CRC Press

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Course Code	Course Title						Core//PE/OE
PC653EC	ELECTRONIC DESIGN AUTOMATION LAB						Core
Pre-requisites	Contact Hours Per Week				CIE	SEE	Credits
DSD with Verilog HDL & VSLI design	L	T	D	P			
	-	-	-	2	20	30	1

Course Objectives: The course is taught with the objectives of enabling the student to:

1. To design and analyze building blocks for a Digital System using HDL platform
2. To understand a Digital System using HDL platform
3. To design and analyze CMOS circuits using back-end platform
4. To draw layout of basic CMOS circuits
5. To Design sequential and combinations circuits using building blocks

Course Outcomes: On completion of this course, the student will be able to :

1. Demonstrate basic building blocks of a Digital System using HDL platform
2. Realize a basic Digital Systems in HDL platform
3. Demonstrate basic building blocks of a Digital System using schematic modeling
4. Demonstrate Layout design and parasitic extraction of CMOS Inverter
5. Evaluate the performance parameters of CMOS inverter at different levels of design abstractions

Experiments

1. Design and Write a Verilog HDL code for BCD to 7-segment decoder for LED and LCD displays and verify.
2. a). Design a 4-bit shift right barrel shifter using 2:1 Mux

   b).Design a 4-bit shift left barrel shifter using 2:1 Mux

   c).Verify both the designs with a test bench.
3. Design a sequence detector for a given sequence in Verilog HDL and verify its function through a test bench and write the output to a file.
4. a). Design a 4-bit CLA and develop HDL code using Generate Loop statements

   b). Design a 4X4 unsigned array Multiplier and develop a HDL code using Generate Loop statements.

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c). Verify both the designs with attest bench.

5. a). Develop a Verilog HDL code for SR and T flip flops with synchronous reset
b). Develop a Verilog HDL code for JK and D flip flops with asynchronous reset
c). Verify them with a suitable test bench

6. Design an N-bit shift register with asynchronous reset and synchronous load/shift controls to operate in the following modes, namely PIPO, SIPO, SISO, PISO. Develop a Verilog HDL code and verify the operation with a test bench.
7. Draw schematic of all CMOS basic gates and simulate using Cadence Schematics tool.
8. Develop basic Building blocks as MUX, Half adder, Full adder, Encoder using CMOS gates in Cadence and verify.
9. Develop a 4-bit Carry look ahead adder and a Carry Bypass adder using Cadence Schematics and verify.
10. Develop a 4-bit Array multiplier with CLA as end accumulator and verify.
11. Develop a 4-bit Carry save multiplier and verify its function using cadence tool.
12. Design and analyze the following CMOS circuits: Inverter using static, ratioed, dynamic and domino logic styles.
13. Design a CMOS inverter and obtain VTC using Cadence Tools.
14. Draw the layout and evaluate the performance of CMOS Inverter and two input CMOS NAND gate.

uggested Reading:

Samir Palnitkar, *"Verilog HDL A Guide to Digital Design and Synthesis,"* 2nd Edition, Pearson Education, 2006

Ming-Bo Lin, *"Digital System Designs and Practices: Using Verilog HDL and FPGA,"* Wiley India Edition, 2008

David A Hodges, H. Jackson and R. A. Saleh, *"Analysis and Design of Digital Integrated Circuits in Deep Submicron Technology"*, 3rd Edition, Tata McGraw Hill, 2007.

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literature survey/ need analysis, the student shall identify the title and define the aim and objectives of mini-project.

- Student is expected to detail out specifications, methodology, resources required, critical issues involved in design and implementation and submit the proposal within first week of the semester.
- The student is expected to exert on design, development and testing of the proposed work as per the schedule.
- Art work and Layout should be made using CAD based software.
- Completed mini project and documentation in the form of mini project report is to be submitted at the end of semester.

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OPEN ELECTIVE - I

Course Code	Course Title					Course Type
OE 601 CE	DISASTER MANAGEMENT					OE
Prerequisite	Contact hours per week			Duration of SEE (Hours)	Scheme of Evaluation	Credits
	L	T	P		CIE / SEE	
	3	-	-	3	30 / 45	3

UNIT- I

Introduction to Disaster: Understanding the Concepts, Definitions and Terminologies used in the field of Disaster Management (i.e. Hazard, Risk, Vulnerability, Resilience, and Capacity Building); Differential impacts of Disasters in terms of Gender, Age, Social Status, Location, Prosperity, Disabilities; Disaster- Development Nexus.

UNIT-II

Types of Hazards and Emerging Trends: Classification, Causes, Consequences and Controls of: Geophysical hazards-Earthquakes, Landslides, Tsunami; Weather related hazards-Meteorological (Cyclones, and Storm- surge), Hydrological (Floods, Droughts, Avalanches), Climatological (Wildfire, Cold & Heat Waves); Biological hazards-Epidemic & Pandemics; Technological hazards- Chemical, Industrial, Nuclear; Man-made hazards-Structural Failure, Fire, Transportation accidents, Terrorism and Wars; Emerging Disasters- Urban Areas, Climate Change; Regional and Global Trends-loss of life & Property in various hazards

UNIT- III

Disaster Management Cycle And International Framework: Disaster Management Cycle: Pre-Disaster – Risk Assessment and Analysis, Risk Mapping, zonation and Micro-zonation, Prevention and Mitigation of Disasters, Early Warning System; Preparedness, Capacity Building; Awareness; During Disaster –Evacuation – Disaster Communication – Search and Rescue– Emergency Operation Centre – Incident Command System – Relief and Rehabilitation; Post-disaster – Damage and Needs Assessment, Restoration of Critical Infrastructure – Early Recovery– Reconstruction and Redevelopment; Paradigm Shift in Disaster Management: International Decade for Natural Disaster Reduction; Yokohama Strategy; Hyogo Framework of Action

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UNIT-IV

Disaster Risk Management in India: Disaster Profile of India – Mega Disasters of India and Lessons Learnt; Disaster Management Act 2005 – Institutional and Financial Mechanism; National Policy on Disaster Management; National Guidelines and Plans on Disaster Management; Role of Government (local, state and national), Non-Government and Inter-governmental Agencies

UNIT-V

Technological Approaches to Disaster Risk Reduction: Geo-informatics in Disaster Management (RS, GIS, GPS and RS); Technological in Disaster Communication System (Early Warning and Its Dissemination), rescue and restoration of services; Disaster Safe Designs and Constructions; Application of technology and innovations for Structural and non structural Mitigation; Science & Technology Institutions for Disaster Management in India

Suggested Reading:

1. Coppola D P. 2007. Introduction to International Disaster Management, Elsevier Science (B/H), London.
2. Manual on natural disaster management in India, M C Gupta, NIDM, New Delhi
3. An overview on natural & man-made disasters and their reduction, R K Bhandani, CSIR, New Delhi
4. World Disasters Report, 2009. International Federation of Red Cross and RedCrescent, Switzerland
5. Disasters in India Studies of grim reality, Anu Kapur & others, 2005, 283 pages, Rawat Publishers, Jaipur
6. National Disaster Management Policy, 2009, GoI.
7. Publications of National Disaster Management Authority (NDMA) on Various Templates and Guidelines for Disaster Management

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Course Code	Course Title					Course Type
OE 602 CE	ROAD SAFETY ENGINEERING					OE
Prerequisite	Contact hours per week			Duration of SEE (Hours)	Scheme of Evaluation	Credits
	L	T	P		CIE / SEE	
	3	-	-	3	30 / 45	3

Course Objectives:

1. To introduce the fundamentals of road safety and road safety audit.
2. To get familiarized with various road safety techniques, measures and their applications.
3. To be able to understand and evaluate various traffic control devices.
4. Familiarize with traffic management techniques.
5. To examine and analyze the incident management process.

Course Outcomes:

1. Analyze Accident data.
2. Plan and design of road safety improvement programs
3. Apply the principles of road safety in urban transport
4. Apply traffic management techniques
5. Able to plan effective incident management program

UNIT-I

Road accidents: Causes, scientific investigations and data collection, analysis of individual accidents to arrive at real causes, statistical methods of analysis of accident data, Basic concepts of road accident statistics, safety performance function: The empirical Bayes method identification of hazards road location. Application of computer analysis of accident data.

UNIT-II

Safety in Road Design: Operating the road network for safety, highway operation and counter measures, road safety audit, principles-procedures and practice, code of good practice and checklists, vehicle design factors & driver characteristics influencing road safety

UNIT-III

Road Signs and Traffic Signals: Classification, Location of signs, measures of sign effectiveness, Types of visual perception, sign regulations, sign visibility, sign variables, Text versus symbols, Road marking: Role of road marking, classification, visibility. Traffic signals: need, Signal face illumination and location of signals, factors affecting signal design, pedestrian's safety, fixed and vehicle actuated signals. Design of signals, area traffic control, delineators, traffic impact attenuators, road side rest areas, safety barriers, traffic aid posts

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UNIT-IV

Traffic Management Techniques: Integrated safety improvement and traffic calming schemes, speed and load limit, traffic lights, safety cameras, tests on driver and vehicles, pedestrian safety issues, parking, parking enforcement and its influence on accidents, travel demand management, methods of traffic management measures: restriction of turning movements, One way streets, tidal flow operation methods, exclusive bus lanes and closing side-streets; latest tools and techniques used for road safety; legislation, enforcement, education and propaganda.

UNIT-V

Incident Management: Introduction, characteristics of traffic incidents types of incidents, impacts, incident management process, incident traffic management; application of ITS: Motorist information, equipment used; planning effective incident management program, best practice in incident management programs. National importance of survival of transpiration systems during and after all natural disasters especially cyclones, earthquakes, floods etc and manmade disasters like sabotage, terrorism etc.

Suggested Reading:

1. Khanna, S.K., Justo, C.E.G and Veeraragavan, A, 'Highway Engineering', Revised 10th Edition, Nem Chand & Bros, 2017.
2. KadiyaliL.R,.Traffic Engineering and Transport planning, 9th Edition, Khanna Tech Publishers, 2013.
3. Donald Drew, Traffic Flow Theory Chapter 14 in Differential Equation Models, Springer, 1983
4. C. Jotinkhisty and B. Kent Lall, Transportation Engineering – An Introduction, 3rd Edition, Pearson publications, 2017
5. Rune Elvik, Alena Hoye, TrulsVaa, Michael Sorenson, Handbook of Road Safety measures, second Edition, Emerald Publishing, 2009

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Course Title						Course Type
PYTHON PROGRAMMING						OE
Contact hours per week			Duration of SEE (Hours)	Scheme of Evaluation		Credits
L	T	P		CIE	SEE	
3	-	-	3	30	45	3

Course Objectives:

1. To know the basics of Programming
2. To convert an algorithm into a Python program
3. To construct Python programs with control structures.
4. To structure a Python Program as a set of functions
5. To use Python data structures-lists, tuples, dictionaries.
6. To do input/output with files in Python.
7. To construct Python programs as a set of objects.

Course Outcomes:

1. Develop algorithmic solutions to simple computational problems.
2. Develop and execute simple Python programs.
3. Develop simple Python programs for solving problems.
4. Structure a Python program into functions.
5. Represent compound data using Python lists, tuples, dictionaries.
6. Read and write data from/to files in Python Programs

UNIT-I

Introduction to Computing and Problem Solving: Fundamentals of Computing – Computing Devices – Identification of Computational Problems – Pseudo Code and Flowcharts – Instructions – Algorithms – Building Blocks of Algorithms.

Introduction to Python Programming: Python Interpreter and Interactive Mode– Variables and Identifiers – Arithmetic Operators – Values and Types – Statements, Reading Input, Print Output, Type Conversions, The type() Function and Is Operator, Dynamic and Strongly Typed Language.

Control Flow Statements: The if, The if...else,The if...elif...else Decision Control Statements, Nested if Statement, The while Loop, The for Loop, The continue and break Statements

UNIT-II

Functions: Built-In Functions, Commonly Used Modules, Function Definition and Calling the Function, The return Statement and void Function, Scope and Lifetime of Variables, Default Parameters, Keyword Arguments, Command Line Arguments.

Strings: Creating and Storing Strings, Basic String Operations, Accessing Characters in String

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Index Number, String Slicing and Joining, String Methods, Formatting Strings.

Lists: list operations, list slices, list methods, list loop, mutability, aliasing, cloning lists, list parameters; Tuples: tuple assignment, tuple as return value; Dictionaries: operations and methods;

advanced list processing - list comprehension; Illustrative programs: selection sort, insertion sort, merge sort, histogram.

UNIT-III

Files and Exception: Text files, reading and writing files, format operator; command line arguments, errors and exceptions, handling exceptions, modules, packages; Illustrative programs: word count, copy file.

Strings: Basic String Operations, String Slicing, Testing, Searching, and Manipulating Strings

Dictionaries and Sets: Dictionaries, Sets, Serializing Objects.

UNIT-IV

Object-Oriented Programming: Classes and Objects, Creating Classes in Python, Creating Objects in Python, The Constructor Method, Classes with Multiple Objects, Class Attributes versus Data Attributes, Encapsulation, Inheritance The Polymorphism.

Functional Programming: Lambda. Iterators, Generators, List Comprehensions.

UNIT-V

GUI Programming: Graphical User Interfaces, Using the tkinter Module, Display text with Label Widgets, Organizing Widgets with Frames, Button Widgets and Info Dialog Boxes, Getting Input with Entry Widge Using Labels as Output Fields, Radio Buttons, Check Buttons.

Suggested Readings:

1. Richard L. Halterman, "*Learning To Program With Python*", Copyright © 2011.
2. Dr. Charles R, "*Python for Everybody, Exploring Data Using Python 3*", Severance. 2016.
3. Gowrishankar S., Veena A, "*Introduction to Python Programming*", CRC Press, Taylor & Francis Group, 2019.
4. Allen B. Downey, "*Think Python: How to Think Like a Computer Scientist*", 2nd Edition, Shroff O"Reilly Publishers, 2016

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Course Code	Course Title					Course Type
OE 602 CS	CYBER SECURITY					OE
Prerequisite	Contact hours per week			Duration of SEE (Hours)	Scheme of Evaluation		Credits
	L	T	P		CIE	SEE	
	3	-	-	3	30	45	3

Course Objectives:

1. Learn the various threats in networks and security concepts.
2. Apply authentication applications in different networks.
3. Understand security services for email.
4. Awareness of firewall and IT laws and policies.

Course Outcomes:

1. Understand the various network threats
2. Analyze the forensic tools for evidence collection
3. Apply the firewalls for threat analysis

UNIT-I

Ethical hacking, Attack Vectors, Cyberspace and Criminal Behaviour, Clarification of Terms, Traditional Problems associated with Computer Crimes, Realms of Cyber world, brief history of the internet, contaminants and destruction of data, unauthorized access, computer intrusions, white-collar crimes, viruses and malicious code, virus attacks, pornography, software piracy, mail bombs, exploitation, stalking and obscenity in internet, Cyber psychology, Social Engineering.

UNIT-II

Introduction to Digital forensics, Forensic software and handling, forensic hardware and handling, analysis and advanced tools, forensic technology and practices, Biometrics: face, iris and fingerprint recognition, Audio-video evidence collection, Preservation and Forensic Analysis.

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UNIT-III

Investigation Tools, e-discovery, EDRM Models, digital evidence collection and preservation, email investigation, email tracking, IP tracking, email recovery, search and seizure of computer systems, password cracking.

UNIT-IV

Forensic Analysis of OS artifact, Internet Artifacts, File System Artifacts, Registry Artifacts, Application Artifacts, Report Writing, Mobile Forensic- identification, collection and preservation of mobile evidences, social media analysis, data retrieval, Email analysis from mobile phones.

UNIT-V

Ethics, Policies and IT Act

Basics of Law and Technology, Introduction to Indian Laws, Scope and Jurisprudence, Digital Signatures, E Commerce-an Introduction, possible crime scenarios, law coverage, data interchange, mobile communication development, smart card and expert systems Indian Laws, Information Technology Act 2000, Indian Evidence Act, India Technology Amendment Act 2008, Indian Penal Code , Computer Security Act 1987, National Information Infrastructure Protection Act 1996, Fraud Act 1997, Children Online Protection Act 1998, Computer Fraud and Abuse Act 2001, Intellectual Property, IP Theft, Copyright, Trademark, Privacy and Censorship, Introduction to Cyber Ethics, rights over intellectual property, Corporate IT Policy Formulations, Compliance Auditing.

Suggested Readings:

1. Charles P. Fleeger, "*Security in Computing*", Prentice Hall, New Delhi, 2009.
2. BehrouzA.Forouzan, "*Cryptography & Network Security*", Tata McGraw Hill, India, New Delhi, 2009.
3. William Stallings, "*Cryptography and Network Security*", Prentice Hall, New Delhi, 2006.
4. Chalie Kaufman, Radia Perlman, Mike Speciner, "*Network Security: Private Communication in a Public Network*", Pearson Education, New Delhi, 2004.
5. Neal Krawetz, "*Introduction to Network Security*", Thomson Learning, Boston, 2007.
6. Bruce Schneier, "*Applied Cryptography*", John Wiley & Sons, New York, 2004.

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Course Code	Course Title						Core//PE/OE
OE603CS	DEEP LEARNING						PE-III
Pre-requisites	Contact Hours Per Week				CIE	SEE	Credits
	L	T	D	P			
-	3	-	-	-	30	45	3

Course Objectives :

The course is taught with the objectives of enabling the student to:

1. To understand complexity of Deep Learning algorithms and their limitations.
2. To understand modern notions in data analysis oriented computing.
3. To apply Deep Learning algorithms in practical applications.
4. To perform experiments in Deep Learning using real-world data.

Course Outcomes :

The student will be able to

1. Understand the concepts of Neural Networks, its main functions, operations and the execution pipeline
2. Implement.
3. Learn topics such as deep learning algorithms, understand neural networks and traverse the layers of data abstraction Convolutional neural networks, recurrent neural networks, training deep networks and modifications Build deep learning models in PyTorch and interpret the results

UNIT-I

Artificial Neural Networks: Introduction, Perceptron, XOR Gate ,Perceptron Training Rule, Activation Functions.

Linear Neural Networks: Linear Regression, Implementation of Linear Regression, Softmax Regression, The Image Classification Dataset , Implementation of Softmax Regression.

UNIT-II

Multilayer Perceptrons:

Multilayer Perceptrons, Implementation of Multilayer Perceptrons, Model Selection, Underfitting and Overfitting, Weight Decay, Dropout, Forward Propagation, Backward Propagation, and Computational Graphs, Numerical Stability and Initialization, Considering the Environment, Predicting House Prices on Kaggle.

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Optimization Algorithms: Optimization and Deep Learning, Convexity, Gradient Descent, Stochastic Gradient Descent, Mini batch Stochastic Gradient Descent, Momentum, Adagrad, RMS Prop, Ada delta, Adam, Learning Rate Scheduling.

UNIT-III

Introduction to Convolutional Neural Networks

Introduction to CNNs, Kernel filter, Principles behind CNNs, Multiple fillters,

Modern Convolutional Neural Networks

Deep Convolutional Neural Networks (AlexNet), Networks Using Blocks (VGG), Network in Network (NiN), Networks with Parallel Concatenations (GoogLeNet), Batch Normalization, Residual Networks (ResNet), Densely Connected Networks (DenseNet).

UNIT-IV

Recurrent Neural Networks: Sequence Models, Text Preprocessing, Language Models and the Dataset, Recurrent Neural Networks, Implementation of Recurrent Neural Networks from Scratch, Concise Implementation of Recurrent Neural Networks, Back propagation Through Time.

Modern Recurrent Neural Networks: Gated Recurrent Units (GRU), Long Short Term Memory (LST), Deep Recurrent Neural Networks, Bidirectional Recurrent Neural Networks, Machine Translation and the Dataset, Encoder-Decoder Architecture, Sequence to Sequence, Beam Search.

UNIT-V

Auto Encoders: Types of Auto Encoders and its applications

Generative Adversarial Networks: Generative Adversarial Network, Deep Convolutional Generative Adversarial Networks

Suggested Readings:

1. Good fellow, I., Bengio, Y., and Courville, A., "*Deep Learning*", MIT Press, 2016.

2. Aston Zhang, Zachary C. Lipton, Mu Li, and Alexander J. Smola, "*Dive into Deep Learng.*

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Course Code	Course Title					Course Type
OE 601EC	VERILOG HDL					OE
Prerequisite	Contact hours per week			Duration of SEE (Hours)	Scheme of Evaluation	Credits
	L	T	P		CIE / SEE	
	3	-	-	3	30 / 45	3

Course Objectives:

1. Tofamiliarizewithvariousmodelingstyles:structural,dataflowandbehavioralofVerilog HDL
2. Todevelopcombinationalandsequentialcircuitsusingvariousmodelingstylesof Verilog HDL
3. TodesignanddevelopVerilogHDLmodelsofcombinationalandsequentialcircuits
4. To learn Synthesis and FPGA design flow
5. Todesignanddevelopreal timeapplications:Booth'smultiplier,Divider,hardwired control for basic CPU, FIR filter

Course Outcomes:

1. Implement and distinguish different Verilog HDL modeling styles.
2. Construct and analyze Verilog HDL models of combinational and sequential circuits.
3. Design and develop Verilog HDL modeling and test bench for digital systems for the given specifications.
4. Outline FPGA design flow and timing analysis.
5. Understand implementation of real time applications.

UNIT-I

Introduction to HDL: Overview and Importance of HDLs, Differences between HLL, HDL and ALP. Design methodologies, Modules, Lexical Conventions, Number Specifications, Strings, Identifiers and Keywords Data types, System task and compiler Directives, Port declaration and port connection rules

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UNIT-II

Structural and Dataflow Modeling: gate-level modeling, delays, hazards, dataflow modeling: Continuous Assignments, Delays, Expressions, Operators and Operands, Operator Types and Design Examples.

UNIT-III

Behavioral Modeling: Structured Procedures, Procedural Assignments, Timing Controls, Conditional Statements, multi-way branching, Loops, Sequential and Parallel blocks, Generate blocks. Combinational, sequential logic modules Simulation: Types of Simulation, Event driven Simulation and Cycle Based Simulation; design examples.

UNIT-IV

Synthesis and Verification: Tasks and Functions: Differences between Tasks and Functions, Tasks and Functions. Verilog HDL synthesis, synthesis, Application Specific IC (ASIC) and Field Programmable Gate Array (FPGA) design flow. Verification: Timing analysis and Test bench design. Design examples.

UNIT-V

Real time implementations: Fixed-Point Arithmetic modules: Addition, Multiplication, Division, Arithmetic and Logic Unit (ALU), Timer, Universal Asynchronous Receiver and Transmitter (UART), DSP modules: FIR and IIR filters, CPU design: Data path and control units.

Suggested Reading:

1. Samir Palnitkar, —*Verilog HDL A Guide to Digital Design and Synthesis*,l 2nd Edition, Pearson Education, 2006..
2. Ming-BoLin,—*DigitalSystemDesignsandPractices:UsingVerilogHDLandFPGA*,l Wiley India Edition, 2008
3. J.Bhasker,—*AVerilogHDLPrimer*,l2ndEdition,BSPublications, 2001

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Course Code	Course Title						Course Type	
OE 602 EC	PRINCIPLES OF ELECTRONIC COMMUNICATION SYSTEMS						OE	
Prerequisite	Contact hours per week			Duration of SEE (Hours)	Scheme of Evaluation		Credits	
	L	T	P		CIE	SEE		
	3	-	-	3	30	45	3	

Course Objectives:

1. Provide an introduction to fundamental concepts in the understanding of Electronic communications systems
2. Provide an introduction to network model and some of the network layers including physical layer, data link layer, network layer and transport layer
3. Provide an introduction to the evolution of wireless systems and current wireless technologies
4. Provide an introduction to fundamental concepts in the understanding of Telecommunication and optical communications systems
5. Provide an introduction to fundamental concepts in Analog and Digital Communications

Course Outcomes:

1. Understand the working of analog and digital communication systems.
2. Understand the Data Communication and Networking
3. Understand the concepts of modulation and demodulations
4. Understand the evolution of communication technologies from traditional telephony systems to modern wireless communication systems
5. Understand the principles of optical communications systems

UNIT- I

Introduction to communication systems: Electromagnetic Frequency Spectrum, Signal and its representation, Elements of Electronic Communications System, Types of Communication Channels, Signal Transmission Concepts-Baseband transmission and Broadband transmission, Communication parameters-Transmitted power, Channel bandwidth and Noise, Need for modulation Signal Radiation and Propagation-Principle of electromagnetic radiation, Types of

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Antennas, Antenna Parameters and Mechanisms of Propagation

UNIT-II

Analog and Digital Communications: Amplitude modulation and demodulation, FM modulation and demodulation, Digital converters, Digital modulation schemes – ASK, FSK, PSK, QPSK, Digital demodulation

UNIT- III

Data Communication and Networking: Network Models, OSI Model, Data Link Layer – Media Access control, Ethernet, Network Layer – Internet Protocol (IPv4/IPv6), Transport Layer – TCP, UDP

UNIT-IV

Telecommunication Systems: Telephones, Telephone system, Paging systems, Internet Telephony. Optical Communications: Optical Principles, Optical Communication Systems, Fiber Optic Cables, Optical Transmitters & Receivers, Wavelength Division Multiplexing

UNIT-V

Wireless Communications: Evolution of Wireless Systems: AMPS, GSM, CDMA, WCDMA, And OFDM. Current Wireless Technologies: Wireless LAN, Bluetooth, PAN and ZigBee, Infrared wireless, RFID communication, UWB, Wireless mesh networks, Vehicular adhoc networks

Suggested Reading:

1. Louis E. Frenzel, "Principles of Electronic Communication Systems", 3e, McGraw Hill publications, 2008.
2. Behrouz A. Forouzan,"Data Communications and Networking", 5e TMH, 2012.
3. Kennady, Davis, "Electronic Communications systems", 4e, TMH, 1999.
4. Keiser Gerd "Optical Fiber Communication (SIE)",5th Edition, McGraw Hill Education India,2017.
5. Simon Haykin, "Communication Systems", 5th Edition, Wiley publications, 2006

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Course Code	Course Title						Course Type
07 A01 FE	APPLICATIONS OF ELECTRICAL ENERGY						OE
Prerequisite	Contact hours per week			Duration of SEE	Scheme of Evaluation		Credits
	L	T	P	(Hours)	CIE	SEE	
	3	-	-	3	30	45	3

Course Objectives:

1. To introduce the students and understand Utilization of electrical energy for various applications like industrial heating.
2. To understand various techniques of electric welding and types of batteries.
3. To understand the concept of illumination and study about the laws of illumination.
4. To know the applications of various lamps to factory lighting, street lighting etc.
5. To understand the concept of electric traction including speed – time curves of different traction services.

Course Outcomes:

1. Identify a suitable heating scheme for a given application.
2. Identify proper welding technique and various characteristics of batteries.
3. Study the nature and production of light and laws related to illumination.
4. Classify types of electric light sources based on nature and operation and their objectives, performance and reliability.
5. Determine the speed-time characteristics of various traction services and also estimate the energy consumption levels at various modes of operation.

UNIT- I

Industrial Heating: Advantages and methods of electric heating. Description, operation and performance of resistance ovens, Design of heating element. High frequency heating, Induction Heating, Induction furnaces, Core type, Coreless furnaces, Dielectric heating. Electric Arc furnaces, Direct Arc furnace, Indirect Arc furnaces.

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UNIT-II

Electric welding: Classification of electric welding, welding transformer and its rating, various types of Electric arc welding and electric resistance welding.

Batteries: Lead acid batteries, SMF batteries, Construction and maintenance, Charging and rating of batteries.

UNIT- III

Illumination: Introduction, nature and production of light, Sensitivity of the eye, Units of light. The inverse square law and cosine law, Solid angle, Lighting calculations, Determination of M.S.C.P, Rousseau"s construction.

UNIT-IV

Types of lamps - Discharge lamps, Sodium vapour lamps, Mercury vapour lamps, Fluorescent lamp and LED lamps. Starting and power factor corrections, stroboscopic effects, Application to factory lighting, Street lighting and Flood lighting.

UNIT-V

Electric Traction: System of Electric Traction, Transmission of drive, Systems of track electrification, Traction mechanics, Speed time curves, Tractive effort, Power of Traction motor, Specific energy consumption, Mechanics of train movement, Coefficient of adhesion.

Suggested Reading:

1. Partab H, Art and Science of Utilization of Electric Power, Dhanpat Rai & Sons, 1997.
2. K.B. Raina & S.K. Bhattacharya, Electrical Design, Estimating 1. and Costing, Wiley Eastern Ltd., 1991.
3. Partab H, Modern Electric Traction, Dhanpat Rai & Sons, 2000.
4. B.L.Theraja, A Text Book of Electrical Technology, S.Chand & Company Ltd, Vol-I.

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Course Code	Course Title					Course Type
OE 602 EE	ELECTRICAL SAFETY MANAGEMENT					OE
Prerequisite	Contact hours per week			Duration of SEE	Scheme of Evaluation	
	L	T	P	(Hours)	CIE	SEE	Credits
	3	-	-	3	30	45	3

Course Objectives:

a. Understand electrical safety measures, the hazards associated with electric current, and voltage identify different types of electrical shocks.
b. Understand installation work of electrical plant and equipment. Safety during installation of outdoor switchyard equipment, safety during installation of electrical rotating machines.
c. Understand procedure of domestic wirings to handle different domestic electrical appliances. Procedure of Agricultural pump installation.
d. Identifies different hazardous zones, classification of equipment enclosure for various hazardous gases, importance of earthing system. Understand Management Safety Policy.
e. Understand standards on electrical safety, different IE Rules and Acts.

Course Outcomes:

1. Explain the objectives and precautions of Electrical safety, effects of shocks and their prevention.
2. Summarize the safety aspects during installation of plant and equipment.
3. Describe the electrical safety in residential, commercial and agricultural installations.
4. Describe the various Electrical safety in hazardous areas, Equipment earthing and system neutral earthing.
5. State the electrical systems safety management and IE rules.

UNIT- I

Introduction to electrical safety, shocks and their prevention: Terms and definitions, objectives of safety and security measures, Hazards associated with electric current, and voltage, who is exposed, principles of electrical safety, Approaches to prevent Accidents, scope of subject electrical safety.

Primary and secondary electrical shocks, possibilities of getting electrical shock and its severity, medical analysis of electric shocks and its effects, shocks due to flash/ Spark over's, prevention of shocks, safety precautions against contact shocks, flash shocks, burns, residential buildings and shops.

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UNIT-II

Safety during installation of plant and equipment: Introduction, preliminary preparations, preconditions for start of installation work, during, risks during installation of electrical plant and equipment, safety aspects during installation, field quality and safety during erection, personal protective equipment for erection personnel, installation of a large oil immersed power transformer, installation of outdoor switchyard equipment, safety during installation of electrical rotating machines, drying out and insulation resistance measurement of rotating machines.

UNIT- III

Electrical safety in residential, commercial and agricultural installations: Wiring and fitting – Domestic appliances – water tap giving shock – shock from wet wall – fan firing shock – multi-storied building – Temporary installations – Agricultural pump installation – Do's and Don'ts for safety in the use of domestic electrical appliances.

UNIT-IV

Electrical safety in hazardous areas: Hazardous zones – class 0,1 and 2 – spark, flashovers and corona discharge and functional requirements – Specifications of electrical plants, equipment for hazardous locations – Classification of equipment enclosure for various hazardous gases and vapours – classification of equipment/enclosure for hazardous locations.

Equipment earthing and system neutral earthing: Introduction, Distinction between system grounding and Equipment Grounding, Equipment Earthing, Functional Requirement of earthing system, description of a earthing system, neutral grounding (System Grounding), Types of Grounding, Methods of Earthing Generators Neutrals.

UNIT-V

Safety management of electrical systems: Principles of Safety Management, Management Safety Policy, Safety organization, safety auditing, Motivation to managers, supervisors, employees.

Review of ie rules and acts and their significance: Objective and scope – ground clearances and section clearances – standards on electrical safety - safe limits of current, voltage –Rules regarding first aid and fire fighting facility. The Electricity Act, 2003, (Part1, 2, 3, 4 & 5).

Suggested Reading:

1. S.Rao, Prof. H.L.Saluja, "Electrical safety, fire safety Engineering and safety management", 1st edition Khanna Publishers. New Delhi, 2016 Reprint.
2. Pradeep Chaturvedi, "Energy management policy, planning and utilization", Concept Publishing company, New Delhi, 1997.

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Course Code	Course Title					Course Type
OE 601 ME	3D PRINTING TECHNOLOGY					OE
Prerequisite	Contact hours per week			Duration of SEE (Hours)	Scheme of Evaluation		Credits
	L	T	P		CIE	SEE	
	3	-	-	3	30	45	3

Course Objectives:

1. To understand the fundamental concepts of 3D Printing, its advantages and limitations.
2. To know the working principle, advantages, disadvantages and applications of liquid, solid and Powder based 3D Printing Technologies.
3. To know the various types of STL file errors and other data formats used in 3D Printing Technology.
4. To know the features of various 3D Printing software's.
5. To know diversified applications of 3D Printing Technologies.

Course Outcomes:

1. Interpret the features of 3D Printing and compare it with conventional methods.
2. Illustrate the working principle of liquid, solid and powder based 3D Printing Technologies.
3. Identify various types of errors in STL file and other data formats used in 3D Printing Technology.
4. Select suitable software used in 3D Printing Technology.
5. Apply the knowledge of various 3D Printing technologies for developing innovative applications.

UNIT- I

Introduction: Prototyping fundamentals: Need for time compression in product development, Historical development, Fundamentals of 3D Printing, 3D Printing Process Chain, Advantages and Limitations of 3D Printing, 3D Printing wheel, Commonly used Terms, Classification of 3D printing processes, Fundamental Automated Processes: Distinction between 3D Printing and Conventional Machining Processes.

UNIT-II

Liquid-based 3D Printing Systems: Stereo Lithography Apparatus (SLA): Models and specifications, Process, working principle, photopolymers, photo polymerization, Layering technology, laser and laser scanning, Applications, Advantages and Disadvantages, Case studies. Polyjet: Models and specifications, Process, working principle, Applications, Advantages and Disadvantages, Case studies Solid-based 3D Printing System: Laminated Object Manufacturing

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(LOM): Models and specifications, Process, working principle, Applications, Advantages and Disadvantages, Case studies. Fused Deposition Modeling (FDM): Models and specifications, Process, working principle, Applications, Advantages and Disadvantages, Case studies.

UNIT- III

Powder Based 3D Printing Systems: Working principle, Specifications, Materials used, Process, Applications, Advantages and Disadvantages, Case studies of the following 3D Printing Technologies like Selective laser sintering (SLS), Selective Laser Melting (SLM) and Direct Metal Laser Sintering (DMLS), Laser Engineered Net Shaping (LENS), Electron Beam Melting (EBM).

UNIT-IV

3D Printing Data Formats & Software: STL Format, STL File Problems, Consequence of Building Valid and Invalid Tessellated Models, STL file Repairs: Generic Solution, Other Translators, Newly Proposed Formats. 3D Printing Software's Features: Magics, Mimics, Solid View, View Expert, 3 D Rhino, 3 D doctor, Flash Print, Object Studio, Cura, ITK Snap, 3-matic, Simplant, 3-matic, Simplant, MeshLab, Ansys for Additive Manufacturing.

UNIT-V

Applications of 3D Printing : Application in Design, Application in Engineering, Analysis and Planning, Aerospace Industry, Automotive Industry, Electronic Industry, Jewellery Industry, Coin Industry, GIS application, Arts and Architecture. RP Medical and Bioengineering Applications: Planning and simulation of complex surgery, Customized Implants & Prosthesis, Design and Production of Medical Devices, Forensic Science and Anthropology, Visualization of Biomolecules. Biopolymers, Packaging, Disaster Management, Entertainment and Sports industry.

Suggested Reading:

1. Chee Kai Chua and Kah Fai Leong, "3D Printing and Additive Manufacturing Principles and Applications" Fifth Edition, World scientific
2. Additive Manufacturing Technologies: 3D Printing, Rapid Prototyping, and Direct Digital Manufacturing- Ian Gibson, David W Rosen, Brent Stucker, Springer, Second Edition, 2010.
3. Rapid Prototyping & Engineering Applications – Frank W.Liou, CRC Press, Taylor & Francis Group, 2011.
4. Rafiq Noorani, Rapid Prototyping: Principles and Applications in Manufacturing, John Wiley & Sons, 2006.
5. NPTEL Course on Rapid Manufacturing. https://nptel.ac.in/courses/112/104/112104265

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Course Code	Course Title						Course Type
OE 602 ME	FINITE ELEMENT METHOD						OE
Prerequisite	Contact hours per week			Duration of SEE (Hours)	Scheme of Evaluation		Credits
	L	T	P		CIE	SEE	
	3	-	-	3	30	45	3

Course Objectives:

1. To understand the theory and application of the finite element method for analyzing structural systems.
2. To learn Approximation theory for structural problems as the basis for finite element methods.
3. To learn formulations for a variety of elements in one, two, and three dimensions. Implementations of element formulations will be examined using Matlab.
4. To understand modeling and analysis of structures using planar, solid, and plate elements

Course Outcomes:

1. Demonstrate a basic understanding of the concepts, mathematical formulation and numerical implementation.
2. Demonstrate the ability to invoke appropriate assumptions, select proper elements and develop FEA models that adequately and efficiently represent physical systems.
3. Underlying the FEA as applied to solid mechanics.
4. Solve 2D vector variable problems and analyze higher order elements and its applications.
5. Create his/her own FEA computer programs using Matlab to solve simple engineering problems.

UNIT- I

Introduction: Historical Background – Mathematical Modeling of field problems in Engineering – Governing Equations – Discrete and continuous models – Boundary, Initial and Eigen Value problems– Weighted Residual Methods – Variational Formulation of Boundary Value Problems – Ritz Technique – Basic concepts of the Finite Element Method.

UNIT-II

One-Dimensional Problems: One Dimensional Second Order Equations – Discretization – Element types- Linear and Higher order Elements – Derivation of Shape functions and Stiffness

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matrices and force vectors- Assembly of Matrices – Solution of problems from solid mechanics and heat transfer. Longitudinal vibration frequencies and mode shapes.

UNIT- III
Two Dimensional Scalar Variable Problems: Second Order 2D Equations involving Scalar Variable Functions – Variational formulation –Finite Element formulation – Triangular elements - Shape functions and element matrices and vectors. Application to Field Problems – Thermal problems – Torsion of Non circular shafts –Quadrilateral elements – Higher Order Elements.

UNIT-IV
Two Dimensional Vector Variable Problems: Equations of elasticity – Plane stress, plane strain and axisymmetric problems – Body forces and temperature effects – Stress calculations – Plate and shell elements.

UNIT-V
Isoparametric Formulation: Natural co-ordinate systems – Isoparametric elements – Shape functions for iso parametric elements – One and two dimensions – Serendipity elements – Numerical integration and application to plane stress problems – Matrix solution techniques – Solutions Techniques to Dynamic problems – Introduction to Analysis Software.

Suggested Reading:

1. Tirupathi R. Chandraputla and Ashok, D. Belgundu" Introduction to Finite Elements in Engineering", Pearson Education, 2002, 3rd Edition.
2. Rao S.S., "The Finite Element Methods in Engineering", pergamon Press, 1989.
3. Segerlind, L.J. "Applied Finite Element Analysis", Wiley Publication, 1984.
4. Reddy J.N., "An Introduction to Finite Element Method", McGraw-Hill Company, 1984.

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Course Code	Course Title						Core//PE/OE
OE603ME	FUNDAMENTALS OF ROBOTICS						Core
Pre-requisites	Contact Hours Per Week				CIE	SEE	Credits
	L	T	D	P			
-	3	-	-	-	30	45	3

Course Objectives:

The course is taught with the objectives of enabling the student to:

1. Familiarize students with various robot configurations.
2. Learn to perform forward and inverse kinematics for general robot configurations.
3. Familiarize with various trajectory planning and control techniques.
4. Will learn to integrate various components in to a robotic system.

Course Outcomes:

On completion of this course, the student will be able to :

1. Identify and classify various robot configurations with their workspaces & their usage in industry
2. Perform forward and inverse kinematics operations & determine singularity conditions for various robot configurations
3. Implement various path planning techniques & control algorithms for computing end effectors' motions for generalized robotic tasks
4. Understand and Use appropriate sensors for specified applications Interface various hardware and software components to develop robotic systems for industry including the effects of multiple finger kinematics

UNIT–I

Brief History, Types of robots, Overview of robot subsystems, resolution, repeatability and accuracy, Degrees of freedom of robots, Robot configurations and concept of workspace, Mechanisms and transmission, End effectors and Different types of grippers, vacuum and other methods of gripping. Pneumatic, hydraulic and electrical actuators, applications of robots, specifications of different industrial robots.

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UNIT-II

Rotation matrices. Euler angle and RPY representation, Homogeneous transformation matrices, Denavit-Hartenberg notation. Direct kinematics. Derivation of DH parameters for various robot configurations. Representation of absolute position and orientation in terms of joint parameters.

UNIT-III

Inverse Kinematics, direct v/s inverse kinematics, inverse orientation, inverse locations, Singularities. Determination of Singular conditions for various common robot configurations, Jacobian

UNIT-IV

Trajectory Planning: joint interpolation, task space interpolation, execution of user specified tasks. Independent joint control, PD and PID feedback, Computed torque control

UNIT – V

Sensors: types of sensors, tactile & non tactile sensors, sensors to measure Position, velocity & acceleration measurement, Optical encoders. Range and Proximity sensing, acoustic, pneumatic, Hall effect sensor, Eddy current sensors, Force and Torque sensors. Vision: Image acquisition, types & components of vision system, Image representation, digitization, binary, gray scale, RGB representation, Image processing, Image segmentation, image smoothening, object descriptors, object recognition. Robots used in general applications like material handling, process applications, assembly operations, inspection applications, healthcare, entertainment.

Suggested Reading:

1. Nagrath and Mittal, "Robotics and Control", Tata McGraw-Hill, 2003.
2. Spong and Vidhyasagar, "Robot Dynamics and Control", John Wiley and sons, 2008.
3. S K Saha, "Introduction to Robotics ", 2nd edition, TMH, 2013
4. Harry Asada & Slottine "Robot Analysis& Control" , Wiley Publications, 2014
5. Fu. K.S, Gonzalez, R.C., Lee, C.S.G, Robotics, control, sensing, Vision and Intelligence, McGraw Hill International, 1987
6. A Mathematical Introduction to Robotic Manipulations- Richard M. Murray, Zexiang Li, S.Shankar Sastry CRC Press.Inc. 1st edition, 1994

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Course Code	Course Title					Course Type
OE601MB	PRINCIPLES OF MANAGEMENT					OE
Prerequisite	Contact hours per week			Duration of SEE (Hours)	Scheme of Evaluation		Credits
	L	T	P		CIE	SEE	
	3	-	-	3	30	45	3

COURSE OBJECTIVES: This course aims to

1. Understand basic fundamentals and insights of management.
2. Understand the nature and purpose of planning.
3. Gain the knowledge about the frame work of organizing.
4. Understand the essence and significance of directing.
5. Recognize the importance of controlling and its outcomes .

COURSE OUTCOMES: After the completion of this course, the student will be able

1. Identify and evaluate the principles of management
2. Demonstrate the ability to have an effective and realistic planning
3. Identify the nature and the type of organization
4. Apply the tools and techniques of directing
5. Explain and evaluate the necessity for controlling and further refinement of an organization

UNIT – I

Management: Concept of Management, Management- Science or Art, Manager vs Entrepreneur; Evolution of Management- Basic management theories by FW Taylor, Henry Fayol; Managerial Roles and Skills; Types of Business Organizations - Sole Proprietorship, Partnership, Company; How startups are built- An overview. Organization Culture and Environment; Contemporary Management Issues and Challenges.

UNIT - II Planning:

Nature and Purpose of Planning, Planning Process, Types of Plans, Environmental Scanning – SWOT and PEST analysis, Objectives, Managing by Objectives (MBO), Strategies – Types of Strategies, The Strategic Planning Process, The TOWS (Threats, Opportunities, Weaknesses and Strengths) Matrix, Planning Tools and Techniques, Planning Plant Location and Layout. Policies- Types. Decision Making – Types of Decision, Decision Making Process.

UNIT – III Organizing:

Nature and purpose of Organizing, formal and informal organization, Organization Structure – Types, line and staff authority, Depart mentation, Span of Control, Centralization and Decentralization,

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Delegation of Authority. Human resource management- HR Planning, Recruitment selection, Training and Development, Performance Management, Career planning and Management.

UNIT - IV Directing:
Principles and elements of direction, Requirement of Effective Direction – Functions of Direction – Supervisor and his Qualities – Supervisor's Role and Functions – Effective Supervision. Individual and group behaviour, motivation- Theories, leadership- Types and Theories of leadership, effective Communication.

UNIT - V Controlling:
Concept, Process of Controlling, Types of control – Budgetary and non-budgetary control techniques – Requirements for effective control. Use of Computers and IT in Management control, Productivity Problems and Management, Control and Performance, Direct and Preventive control, Reporting.

Text Books:

1. Andrew J. Dubrin, "Essentials of Management", 9th edition, Thomson Southwestern, 2012.
2. Harold Koontz and Heinz Weihrich, "Essentials of management: An International & Leadership Perspective", 9th edition, Tata McGraw-Hill Education, 2012.

Suggested Readings:

1. Charles W.L Hill and Steven L McShane, "Principles of Management", Special Indian Edition, McGraw Hill Education, 2007.
2. Don Hellriegel, Susan E. Jackson and John W. Slocum, "Management- A competency-based approach", 11th edition, Thompson South Western, 2008.
3. Harold Koontz and Cyril O'Donnell "Principles of Management'', Tata McGraw Hill, 2017. Stephen P. Robbins, David A.De Cenzo and Mary Coulter, "Fundamentals of management", Prentice Hall of India, 2012.

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